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Proceedings, 3rd International Symposium on Advanced Packaging Materials ,1997 Electrical & **Electronics Abstracts** ,1997 Microelectronics Packaging Handbook R.R. Tummala, Eugene J. Rymaszewski, Alan G. Klopfenstein, 2013-11-27 Electronics has become the largest industry surpassing agriculture auto and heavy metal industries It has become the industry of choice for a country to prosper already having given rise to the phenomenal prosperity of Japan Korea Singapore Hong Kong and Ireland among others At the current growth rate total worldwide semiconductor sales will reach 300B by the year 2000 The key electronic technologies responsible for the growth of the industry include semiconductors the packaging of semiconductors for systems use in auto telecom computer consumer aerospace and medical industries displays magnetic and optical storage as well as software and system technologies There has been a paradigm shift however in these technologies from mainframe and supercomputer applications at any cost to consumer applications at approximately one tenth the cost and size Personal computers are a good example going from 500IMIP when products were first introduced in 1981 to a projected IIMIP within 10 years Thin light portable user friendly and very low cost are therefore the attributes of tomorrow s computing and communications systems Electronic packaging is defined as interconnection powering cool ing and protecting semiconductor chips for reliable systems It is a key enabling technology achieving the requirements for reducing the size and cost at the system and product level Solder Joint Reliability of BGA, CSP, Flip Chip, and Fine Pitch SMT Assemblies John H. Lau, Yi-hsin Pao, 1997 The explosive growth of high density packaging has created a tremendous impact on the electronic assembly and manufacturing industry Ball grid array BGA chip scale package CSP and solder bumped flip chip technologies are taking the lead in this advanced manufacturing process Many major equipment makers and leading electronic companies are now gearing up for these emerging and advanced packaging technologies For these technologies solder is the electrical and mechanical glue and thus solder joint reliability is one of the most critical issues in the development of these technologies This book is a one stop guide to the state of the art of solder joint reliability problem solving methods or choose a creative high performance robust and cost effective design and high yield manufacturing process for their interconnect systems will be able to do so with this unique sourcebook It meets the reference needs of design material process equipment manufacturing quality control product assurance reliability component

packaging vendor marketing and system engineers and technical managers working in electronic packaging and interconnection This book is structured to provide readers with the necessary know how for practical on the job problem solving guidance The book covers the solder joint reliability of BGA CSP flip chip and FPT assemblies completely proceeding from the theoretical basics to applications Specific areas covered include Definition of reliability life distribution failure rate mean time to failure etc Some well known life distributions Accelerated testing Parameter estimation of life distributions Acceleration factors for solders Solder mechanics plasticity creep and constitutive equations Design material and manufacturing processes of BGA CSP flip chip and FTP Failure analysis and root cause of failure for BGA CSP flip chip and FPT solder joints Design for reliability of BGA CSP flip chip and FPT solder joints Solder joint reliability of CBGA PBGA DBGA and TBGA assemblies under thermal fatigue mechanical bending and twisting and shock and vibration conditions solder joint reliability of flip chip e g high temperature and eutectic solder bumped flip chips on ceramic and PCB assemblies under thermal fatigue mechanical pulling shearing bending and twisting and shock and vibration conditions Solder joint reliability of CSP e g LG Semicon s Mitsubishi s Motorola s Tessera s NEC s nitto Denko s and Toshiba s assemblies under thermal fatique and mechanical bending conditions Solder joint reliability of POFP and TSOP assemblies under thermal fatique mechanical bending and twisting and vibration conditions **IEEE/CPMT International Electronic Manufacturing** Technology Symposium : [proceedings]. ,1998 1995 Japan IEMT Symposium ,1996 1996 International Symposium on Microelectronics, 1996 Microelectronics Packaging Handbook Rao Tummala, Eugene J. Rymaszewski, Alan G. Klopfenstein, 1997-01-31 This thoroughly revised and updated three volume set continues to be the standard reference in the field providing the latest in microelectronics design methods modeling tools simulation techniques and manufacturing procedures Unlike reference books that focus only on a few aspects of microelectronics packaging these outstanding volumes discuss state of the art packages that meet the power cooling protection and interconnection requirements of increasingly dense and fast microcircuitry Providing an excellent balance of theory and practical applications this dynamic compilation features step by step examples and vital technical data simplifying each phase of package design and production In addition the volumes contain over 2000 references 900 figures and 250 tables Part I Technology Drivers covers the driving force of microelectronics packaging electrical thermal and reliability It introduces the technology developer to aspects of manufacturing that must be considered during product development Part II Semiconductor Packaging discusses the interconnection of the IC chip to the first level of packaging and all first level packages Electrical test sealing and encapsulation technologies are also covered in detail Part III Subsystem Packaging explores board level packaging as well as connectors cables and optical packaging **IEEE/CPMT International Electronics Manufacturing Technology** Symposium ,2004 7000, The International Journal of Microcircuits and Electronic Packaging **Twenty First** IEEE/CPMT International Electronics Manufacturing Technology Symposium, October 13-15, 1997, Austin, TX,

USA Don Millard, Semiconductor Equipment and Materials International, Components, Packaging & Manufacturing Technology Society, 1997 The IEMT symposium provides a forum for sharing experiences and knowledge based on microelectronic research and development This volume is the result of the 1997 symposium and topics include flip chip and TAB substrate soldering process manufacturing and packaging technology

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