

FCCM 2016

The 24th IEEE International Symposium on
Field-Programmable Custom Computing Machines

May 1-3, Washington DC, USA



Seventh Annual Ieee Symposium On Field Programmable Custom Computing Machines Fccm 99

Kenneth L. Pocek, Jeffrey M. Arnold



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Dynamic Reconfigurable Architectures and Transparent Optimization Techniques Antonio Carlos Schneider Beck Fl.,Luigi Carro,2010-03-10 Dynamic Reconfigurable Architectures and Transparent Optimization Techniques presents a detailed study on new techniques to cope with the aforementioned limitations First characteristics of reconfigurable systems are discussed in details and a large number of case studies is shown Then a detailed analysis of several benchmarks demonstrates that such architectures need to attack a diverse range of applications with very different behaviours besides supporting code compatibility This requires the use of dynamic optimization techniques such as Binary Translation and Trace reuse Finally works that combine both reconfigurable systems and dynamic techniques are discussed and a quantitative analysis of one them the DIM architecture is presented

High Performance Visualization E. Wes Bethel,Hank Childs,Charles Hansen,2012-10-25 Visualization and analysis tools techniques and algorithms have undergone a rapid evolution in recent decades to accommodate explosive growth in data size and complexity and to exploit emerging multi and many core computational platforms High Performance Visualization Enabling Extreme Scale Scientific Insight focuses on the subset of scientific visualization concerned with algorithm design implementation and optimization for use on today s largest computational platforms The book collects some of the most seminal work in the field including algorithms and implementations running at the highest levels of concurrency and used by scientific researchers worldwide After introducing the fundamental concepts of parallel visualization the book explores approaches to accelerate visualization and analysis operations on high performance computing platforms Looking to the future and anticipating changes to computational platforms in the transition from the petascale to exascale regime it presents the main research challenges and describes several contemporary high performance visualization implementations Reflecting major concepts in high performance visualization this book unifies a large and diverse body of computer science research development and practical applications It describes the state of the art at the intersection of scientific visualization large data and high performance computing trends giving readers the foundation to apply the concepts and carry out future research in this area

Adaptable Embedded Systems Antonio Carlos Schneider Beck,Carlos Arthur Lang Lisboa,Luigi Carro,2012-11-27 As embedded systems become more complex designers face a number of challenges at different levels they need to boost performance while keeping energy consumption as low as possible they need to reuse existent software code and at the same time they need to take advantage of the extra logic available in the chip represented by multiple processors working together This book describes several strategies to achieve such different and interrelated goals by the use of adaptability Coverage includes reconfigurable systems dynamic optimization techniques such as binary translation and trace reuse new memory architectures including homogeneous and heterogeneous multiprocessor systems communication issues and NOCs fault tolerance against fabrication defects and soft errors and finally how one can combine several of these techniques together to

achieve higher levels of performance and adaptability The discussion also includes how to employ specialized software to improve this new adaptive system and how this new kind of software must be designed and programmed

Cryptographic Hardware and Embedded Systems - CHES 2000 Cetin K. Koc, Christof Paar, 2000-12-13 This book constitutes the thoroughly refereed post proceedings of the Second International Workshop on Cryptographic Hardware and Embedded Systems CHES 2000 held in Worcester MA USA in August 2000 The 25 revised full papers presented together with two invited contributions were carefully reviewed and selected from 51 submissions The papers are organized in topical sections on implementation of elliptic curve cryptosystems power and timing analysis attacks hardware implementation of block ciphers hardware architectures power analysis attacks arithmetic architectures physical security and cryptanalysis and new schemes and algorithms

Partial Reconfiguration on FPGAs Dirk Koch, 2012-07-25 This is the first book to focus on designing run time reconfigurable systems on FPGAs in order to gain resource and power efficiency as well as to improve speed Case studies in partial reconfiguration guide readers through the FPGA jungle straight toward a working system The discussion of partial reconfiguration is comprehensive and practical with models introduced together with methods to implement efficiently the corresponding systems Coverage includes concepts for partial module integration and corresponding communication architectures floorplanning of the on FPGA resources physical implementation aspects starting from constraining primitive placement and routing all the way down to the bitstream required to configure the FPGA and verification of reconfigurable systems

ESL Design and Verification Grant Martin, Brian Bailey, Andrew Piziali, 2010-07-27 Visit the authors companion site <http://www.electronicssystemlevel.com> Includes interactive forum with the authors Electronic System Level ESL design has mainstreamed it is now an established approach at most of the world's leading system on chip SoC design companies and is being used increasingly in system design From its genesis as an algorithm modeling methodology with no links to implementation ESL is evolving into a set of complementary methodologies that enable embedded system design verification and debug through to the hardware and software implementation of custom SoC system on FPGA system on board and entire multi board systems This book arises from experience the authors have gained from years of work as industry practitioners in the Electronic System Level design area they have seen SLD or ESL go through many stages and false starts and have observed that the shift in design methodologies to ESL is finally occurring This is partly because of ESL technologies themselves are stabilizing on a useful set of languages being standardized SystemC is the most notable and use models are being identified that are beginning to get real adoption ESL DESIGN VERIFICATION offers a true prescriptive guide to ESL that reviews its past and outlines the best practices of today Table of Contents CHAPTER 1 WHAT IS ESL CHAPTER 2 TAXONOMY AND DEFINITIONS FOR THE ELECTRONIC SYSTEM LEVEL CHAPTER 3 EVOLUTION OF ESL DEVELOPMENT CHAPTER 4 WHAT ARE THE ENABLERS OF ESL CHAPTER 5 ESL FLOW CHAPTER 6 SPECIFICATIONS AND MODELING CHAPTER 7 PRE PARTITIONING ANALYSIS CHAPTER 8 PARTITIONING CHAPTER 9 POST

PARTITIONING ANALYSIS AND DEBUG CHAPTER 10 POST PARTITIONING VERIFICATION CHAPTER 11 HARDWARE IMPLEMENTATION CHAPTER 12 SOFTWARE IMPLEMENTATION CHAPTER 13 USE OF ESL FOR IMPLEMENTATION VERIFICATION CHAPTER 14 RESEARCH EMERGING AND FUTURE PROSPECTS APPENDIX LIST OF ACRONYMS Provides broad comprehensive coverage not available in any other such book Massive global appeal with an internationally recognised author team Crammed full of state of the art content from notable industry experts Proceedings, Seventh Annual IEEE Symposium on Field-Programmable Custom Computing Machines Kenneth L. Pocek, Jeffrey M. Arnold, 1999

Field-programmable Logic and Applications, 1999 **Index of Conference Proceedings** British Library. Document Supply Centre, 2003 **Automatic Compilation of C for Hybrid Reconfigurable Architectures** Timothy John Callahan, 2002 **American Book Publishing Record**, 2007 **Proceedings**, 2002 Advances in Computer Systems Architecture, 2005 *Web-Based Management of IP Networks and Systems* Jean-Philippe Martin-Flatin, 2003 Building on the author's extensive experience in industry and government agencies this book proposes a general framework for integrating the management of networked systems in the IP world The Web based Integrated Management Architecture WIMA leverages XML's self description capability to integrate SNMP data and CIM objects in a seamless manner The first part of the book begins with a detailed analysis of SNMP it highlights its strengths and weaknesses and justifies the need for object oriented information models such as CIM Next the author summarizes the different paradigms considered in management research and industry in the past decade compares the suitability of different middleware solutions and concludes that with current technologies Web based management makes a lot of technical sense and is not just trendy The second part goes into software engineering detail and discusses tradeoffs for organizing manager to agent communication In WIMA agents publish the monitoring data and notifications they can send and management applications managers subscribe to them in a semi or fully automated way The same publish subscribe paradigm is used for manager to manager communication when managers are organized hierarchically to manage a large domain or different domains We explain why this solution decreases network overhead and improves scalability Security issues are also investigated The third part of the book presents the high level design of JAMAP a research prototype that implements the main components of the WIMA architecture Its code is freely available and released under the GPL license This prototype is currently being ported to Web Services Finally the author compares WIMA with SNMP WBEM CIM and JMX and concludes that WBEM could evolve into WIMA in the future This book is primarily aimed at software architects and engineers who design and write management applications It should also be of interest to administrators and people who are versed into management architectures

FPGA ..., 2003 *Directory of Published Proceedings*, 2002 **Bioinformatics** Bertil Schmidt, 2011 A compilation of recent approaches from prominent researchers this referencediscusses how to take advantage of bioinformatics applications and algorithmson a variety of modern parallel architectures **Proceedings, the Irish Signals and Systems Conference**

2004 Sakir Sezer, Sean McLoone, Dr. Uwe Krüger, 2004 Over the past 12 years ISSC has been a major forum for engineers and young researchers in Ireland on communications control and DSP The conference has established itself as one of the premier conferences in Ireland addressing all aspects of signals and systems including design implementation algorithms modelling and performance This conference continued this tradition under the auspices of the IEE and for the first time the ISSC conference proceedings are published by the IEE and indexed by INSPEC Tutorial Guide, 2003

Field-Programmable Custom Computing Machines, 2001. FCCM '01. The 9th Annual IEEE Symposium on, 2001

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