

Piecewise Regular Arrays

Application-specific computations

Thomas P. Plaks

Gordon and Breach Science Publishers

Parallel Processing Series

Piecewise Regular Arrays Application Specific Computations

Ed F. Deprettere, Alle-Jan van der Veen



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Piecewise Regular Arrays Thomas P Plaks,1999-02-22 Application specific regular array processors have been widely used in signal and image processing multimedia and communication systems for example in data compression and HDTV One of the main problems of application specific computing is how to map algorithms into hardware The major achievement of the theory of regular arrays is that an algorithm represented as a data dependence graph is embedded into a Euclidean space where the integer points are the elementary computations and the dependencies between computations are denoted by vectors between points The process of mapping an algorithm into hardware is reduced to finding for the given Euclidean space a new coordinate system that can be associated with the physical properties of space and time so called space time The power of the synthesis method is that it provides a bridge between abstract and physical representations of algorithms thus providing a methodological basis for synthesizing computations in space and in time This book will extend the existing synthesis theory by exploiting the associativity and commutativity of computations The practical upshot being a controlled increase in the dimensionality of the Euclidean space representing an algorithm This increase delivers more degrees of freedom in the choice of the space time mapping and leads subsequently to more choice in the selection of cost effective application specific designs Algorithm Engineering for Integral and Dynamic Problems Lucia Rapanotti,2001-01-23

Algorithm engineering allows computer engineers to produce a computational machine that will execute an algorithm as efficiently and cost effectively as possible given a set of constraints such as minimal performance or the availability of technology Addressing algorithm engineering in a parallel setting regular array syntheses offer powerful co

Reconfigurable Technology ,2000 **ICFEM 2000** Shaoying Liu,John McDermid,Michael Gerard Hinchey,2000 Annotation Invited speakers explore the use of mathematics in software engineering legacy code and challenges and results of offering formal verification capabilities for industry standard case tools The other 19 papers cover development structuring algebraic approaches verification formal and informal notations retrenchment the theory and practice of Z and animation There is no subject index Annotation copyrighted by Book News Inc Portland OR *Computer Systems and Software Engineering* Patrick DeWilde,Joos P.L. Vandewalle,2012-12-06 Computer Systems and Software Engineering is a compilation of sixteen state of the art lectures and keynote speeches given at the COMPEURO 92 conference The contributions are from leading researchers each of whom gives a new insight into subjects ranging from hardware design through parallelism to computer applications The pragmatic flavour of the contributions makes the book a valuable asset for both researchers and designers alike The book covers the following subjects Hardware Design memory technology logic design algorithms and architecture Parallel Processing programming cellular neural networks and load balancing Software Engineering machine learning logic programming and program correctness Visualization the graphical computer interface

VLSI Implementations for Image Communications P. Pirsch,2014-06-28 The past few years have seen a rapid growth in

image processing and image communication technologies New video services and multimedia applications are continuously being designed Essential for all these applications are image and video compression techniques The purpose of this book is to report on recent advances in VLSI architectures and their implementation for video signal processing applications with emphasis on video coding for bit rate reduction Efficient VLSI implementation for video signal processing spans a broad range of disciplines involving algorithms architectures circuits and systems Recent progress in VLSI architectures and implementations has resulted in the reduction in cost and size of video signal processing equipment and has made video applications more practical The topics covered in this volume demonstrate the increasingly interdisciplinary nature of VLSI implementation of video signal processing applications involving interactions between algorithms VLSI architectures circuit techniques semiconductor technologies and CAD for microelectronics

Proceedings of the International Conference on Application Specific Array Processors José A. Fortes, Edward A. Lee, Teresa H. Meng, 1992

Subject Guide to Books in Print, 1991

IEEE International Conference on Application-Specific Systems, Architectures and Processors Lothar Thiele (Computer scientist), 1997 These conference papers on computer hardware and design and testing include coverage of miscellaneous applications arithmetic algorithms and architectures DSP architectures systolic algorithms and architectures rapid prototyping design methodologies and compilers

Parallel Processing on VLSI Arrays Josef A. Nossek, 2012-12-06 Guest Editor JOSEF A NOSSEK This is a special issue of the Journal of VLSI Signal Processing comprising eight contributions invited for publication on the basis of novel work presented in a special session on Parallel Processing on VLSI Arrays at the International Symposium on Circuits and Systems ISCAS held in New Orleans in May 1990 Massive parallelism to cope with high speed requirements stemming from real time applications and the restrictions in architectural and circuit design such as regularity and local connectedness brought about by the VLSI technology are the key questions addressed in these eight papers They can be grouped into three subsections elaborating on Simulation of continuous physical systems i e numerically solving partial differential equations Neural architectures for image processing and pattern recognition Systolic architectures for implementing regular and irregular algorithms in VLSI technology The paper by A Fettweis and O Nitsche advocates a signal processing approach for the numerical integration of partial differential equations PDEs It is based on the principles of multidimensional wave digital filters MDWDFs thereby preserving the passivity of energy dissipating physical systems It is particularly suited for systems of PDEs involving time and finite propagation speed The basic ideas are explained using Maxwell's equations as a vehicle for the derivation of a multidimensional equivalent circuit representing the spatially infinitely extended arrangement with only very few circuit elements

The International Conference on Application Specific Array Processors Peter R. Cappello, Catherine Mongenet, Guy-René Perrin, 1995 Papers invited talks and poster sessions from the July 1995 conference address problems and solutions in the field of parallel array algorithms and architectures for special purpose systems Topics include scheduling and mapping techniques design

methodologies array architectures computer arith **VLSI Signal Processing, IV** Howard S. Moscovitz, Kung Yao, Rajeev Jain, 1991 **Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology**, 1997

Algorithms and Parallel VLSI Architectures Ed F. Deprettere, Alle-Jan van der Veen, 1991 **COMPSAC 2001**, 2001

Papers from an October 2001 address such themes as requirements engineering component based development protocols and harmonization quality management software architecture workflow systems and software testing distributed systems UML commercial off the shelf components e learning applicat Tenth International Symposium on System Synthesis, 1997

Computer Aided Systems Theory, 1999 *Comp Euro*, 1992 **EUROMICRO 99, Proceedings of the 25th EUROMICRO Conference**, 1999 Annotation This is a two volume set of the proceedings of the September 1999 conference on the current and future developments in informatics theories and application areas Volume I 80 contributions discusses digital system design architectures and methods and tools Volume II 30 contributions covers music technology and audio processing dependable computing systems software process and product improvement multimedia and telecommunication and network computing Lacks a subject index Annotation copyrighted by Book News Inc Portland OR Advanced Signal-processing Algorithms, Architectures, and Implementations, 1992

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