QUICK-TURNAROUND ASIC DESIGN IN VHDL

Core-Based Behavioral Synthesis

Quick Turnaround Asic Design In Vhdl

Muhammad S. Elrabaa, Issam S. Abu-Khater, Mohamed I. Elmasry

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Quick-Turnaround ASIC Design in VHDL N. Bouden-Romdhane, Vijay Madisetti, J.W. Hines, 2012-12-06 From the Foreword Modern digital signal processing applications provide a large challenge to the system designer Algorithms are becoming increasingly complex and yet they must be realized with tight performance constraints Nevertheless these DSP algorithms are often built from many constituent canonical subtasks e g IIR and FIR filters FFTs that can be reused in other subtasks Design is then a problem of composing these core entities into a cohesive whole to provide both the intended functionality and the required performance In order to organize the design process there have been two major approaches. The top down approach starts with an abstract concise functional description which can be quickly generated On the other hand the bottom up approach starts from a detailed low level design where performance can be directly assessed but where the requisite design and interface detail take a long time to generate In this book the authors show a way to effectively resolve this tension by retaining the high level conciseness of VHDL while parameterizing it to get good fit to specific applications through reuse of core library components Since they build on a pre designed set of core elements accurate area speed and power estimates can be percolated to high level design routines which explore the design space Results are impressive and the cost model provided will prove to be very useful Overall the authors have provided an up to date approach doing a good job at getting performance out of high level design The methodology provided makes good use of extant design tools and is realistic in terms of the industrial design process The approach is interesting in its own right but is also of direct utility and it will give the existing DSP CAD tools a highly competitive alternative The techniques described have been developed within ARPAs RASSP Rapid Prototyping of Application Specific SignalProcessors project and should be of great interest there as well as to many industrial designers Professor Jonathan Allen Massachusetts Institute of Technology Advanced Low-Power Digital Circuit Techniques Muhammad S. Elrabaa, Issam S. Abu-Khater, Mohamed I. Elmasry, 2012-10-28 Advanced Low Power Digital Circuit Techniques presents several novel high performance digital circuit designs that emphasize low power and low voltage operation These circuits represent a wide range of circuits that are used in state of the art VLSI systems and hence serve as good examples for low power design Each chapter contains a brief introduction that serves as a quick background and gives the motivation behind the design Each chapter also ends with a summary that briefly explains the contributions contained therein This makes the book very readable The reader can skim through the chapters very quickly to get a feel for the design problems presented in the book and the solutions proposed by the authors Examples of circuits used in systems where low power is important from reliability and portability points of view such as general purpose and DSP processors are presented in Chapters 2 3 and 4 Chapters 5 and 7 give examples of circuits used in systems where reliability and more system integration are the main driving forces behind lowering the power consumption Chapter 6 gives an example of a general purpose high performance low power circuit design Advanced Low Power Digital Circuit Techniques is a real

designer's book It investigates alternative circuit styles as well as architectural alternatives and gives quantitative results for comparison in realistic technologies Several of the circuits presented have been fabricated so that simulations can be checked The circuits covered are the most important building blocks for many designs so the text will be of direct use to designers MOS designs are covered as well as BiCMOS and there are several novel circuits **Application Specific** Processors Earl E. Swartzlander Jr., 2012-12-06 Application Specific Processors is written for use by engineers who are developing specialized systems application specific systems Traditionally most high performance signal processors have been realized with application specific processors. The explanation is that application specific processors can be tailored to exactly match the usually very demanding application requirements. The result is that no processing power is wasted for unnecessary capabilities and maximum performance is achieved A disadvantage is that such processors have been expensive to design since each is a unique design that is customized to the specific application In the last decade computer aided design systems have been developed to facilitate the development of application specific integrated circuits The success of such ASIC CAD systems suggests that it should be possible to streamline the process of application specific processor design Application Specific Processors consists of eight chapters which provide a mixture of techniques and examples that relate to application specific processing The inclusion of techniques is expected to suggest additional research and to assist those who are faced with the requirement to implement efficient application specific processors. The examples illustrate the application of the concepts and demonstrate the efficiency that can be achieved via application specific processors. The chapters were written by members and former members of the application specific processing group at the University of Texas at Austin The first five chapters relate to specific arithmetic which often is the key to achieving high performance in application specific processors The next two chapters focus on signal processing systems and the final chapter examines the interconnection of possibly disparate elements to create systems The Digital Signal Processing Handbook VIJAY MADISETTI, 1997-12-29 The field of digital signal processing DSP has spurred developments from basic theory of discrete time signals and processing tools to diverse applications in telecommunications speech and acoustics radar and video This volume provides an accessible reference offering theoretical and practical information to the audience of DSP users This immense compilation outlines both introductory and specialized aspects of information bearing signals in digital form creating a resource relevant to the expanding needs of the engineering community It also explores the use of computers and special purpose digital hardware in extracting information or transforming signals in advantageous ways Impacted areas presented include Telecommunications Computer engineering Acoustics Seismic data analysis DSP software and hardware Image and video processing Remote sensing Multimedia applications Medical technology Radar and sonar applications This authoritative collaboration written by the foremost researchers and practitioners in their fields comprehensively presents the range of DSP from theory to application from algorithms to hardware Encyclopedia of Microcomputers Allen Kent, James G. Williams, 1997-05-21

Visual Fidelity Designing Multimedia Interfaces for Active Learning to Xerox Corporation **Engineering Applications of** Bio-Inspired Artificial Neural Networks Jose Mira, Juan V. Sanchez-Andres, 1999-05-19 This book constitutes together with its compagnion LNCS 1606 the refereed proceedings of the International Work Conference on Artificial and Neural Networks IWANN 99 held in Alicante Spain in June 1999 The 91 revised papers presented were carefully reviewed and selected for inclusion in the book This volume is devoted to applications of biologically inspired artificial neural networks in various engineering disciplines The papers are organized in parts on artificial neural nets simulation and implementation image processing and engineering applications Digital Signal Processing Handbook on CD-ROM VIJAY MADISETTI, Douglas Williams, 1999-02-26 A best seller in its print version this comprehensive CD ROM reference contains unique fully searchable coverage of all major topics in digital signal processing DSP establishing an invaluable time saving resource for the engineering community Its unique and broad scope includes contributions from all DSP specialties including telecommunications computer engineering acoustics seismic data analysis DSP software and hardware image and video processing remote sensing multimedia applications medical technology radar and sonar applications Signal Processing Systems for Signal, Image, and Video Technology, 1996 **Encyclopedia of Computer Science and** Technology Allen Kent, James G. Williams, 2000-04-28 Combining Artificial Neural Networks to Symbolic and Algebraic IEEE Workshop on Signal Processing Systems, 1999 1999 IEEE International Conference on Acoustics, computation Speech, and Signal Processing, 1999 **International Aerospace Abstracts** ,1997 IEEE Circuits & Devices ,1999

Application Specific Processors Earl E. Swartzlander, 1997 Application Specific Processors consists of eight chapters which provide a mixture of techniques and examples that relate to application specific processing. The inclusion of techniques is expected to suggest additional research and to assist those who are faced with the requirement to implement efficient application specific processors. The examples illustrate the application of the concepts and demonstrate the efficiency that can be achieved via application specific processors. The chapters were written by members and former members of the application specific processing group at the University of Texas at Austin Digital Systems Design with VHDL and **Synthesis** Kou-Chuan Chang, 1999-05-11 A result of K C Chang's practical experience in both design and as an instructor this book presents an integrated approach to digital design principles processes and implementations to help the reader design much more complex systems within a shorter design cycle Many of the design techniques and considerations illustrated throughout the chapters are examples of viable designs EUROMICRO 99, Proceedings of the 25th EUROMICRO Conference, 1999 Annotation This is a two volume set of the proceedings of the September 1999 conference on the current and future developments in informatics theories and application areas Volume I 80 contributions discusses digital system design architectures and methods and tools Volume II 30 contributions covers music technology and audio processing dependable computing systems software process and product improvement multimedia and telecommunication and network

computing Lacks a subject index Annotation copyrighted by Book News Inc Portland OR **IEEE International** Conference on Application-Specific Systems, Architectures and Processors Lothar Thiele (Computer scientist), 1997 These conference papers on computer hardware and design and testing include coverage of miscellaneous applications arithmetic algorithms and architectures DSP architectures systolic algorithms and architectures rapid prototyping design methodolgies and compilers Electronic Design ,2001 The Engineering Index Annual ,1992 Since its creation in 1884 Engineering Index has covered virtually every major engineering innovation from around the world It serves as the historical record of virtually every major engineering innovation of the 20th century Recent content is a vital resource for current awareness new production information technological forecasting and competitive intelligence The world's most comprehensive interdisciplinary engineering database Engineering Index contains over 10 7 million records Each year over 500 000 new abstracts are added from over 5 000 scholarly journals trade magazines and conference proceedings Coverage spans over 175 engineering disciplines from over 80 countries Updated weekly **Eleventh International Conference on** VLSI Design VLSI Society of India, IEEE Circuits and Systems Society, 1997 Areas covered in this work include physical design synthesis delay test and timing high level synthesis hardware software co design low power design verification VLSI synthesis testability enhancement asynchronous design diagnosis test and fault modelling and mixed signal design

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