Piton

A Mechanically Verified Assembly-Level Language

by

J Strother Moore

Piton A Mechanically Verified Assembly Level Language

Yuan Yu

Piton A Mechanically Verified Assembly Level Language:

   Piton J Strother Moore,2007-08-28 Mountaineers use pitons to protect themselves from falls The lead climber wears a harness to which a rope is tied As the climber ascends the rope is paid out by a partner on the ground As described thus far the climber receives no protection from the rope or the partner However the climber generally carries several spike like pitons and stops when possible to drive one into a small crack or crevice in the rock face After climbing just above the piton the climber clips the rope to the piton using slings and carabiners A subsequent fall would result in the climber hanging from the piton if the piton stays in the rock the slings and carabiners do not fail the rope does not break the partner is holding the rope taut and secure and the climber had not climbed too high above the piton before falling The climber s safety clearly depends on all of the components of the system But the piton is distinguished because it connects the natural to the artificial In 1987 I designed an assembly level language for Warren Hunt s FM8501 verified microprocessor I wanted the language to be conveniently used as the object code produced by verified compilers Thus I envisioned the language as the first software link in a trusted chain from verified hardware to verified applications programs Thinking of the hardware as the rock I named the language Piton   **Logic for Programming, Artificial Intelligence, and Reasoning** Moshe Vardi,Andrei Voronkov,2003-12-01 This book constitutes the refereed proceedings of the 10th International Conference on Logic Programming Artificial Intelligence and Reasoning LPAR 2003 held in Almaty Kazakhstan in September 2003 The 27 revised full papers presented together with 3 invited papers were carefully reviewed and selected from 65 submissions The papers address all current issues in logic programming automated reasoning and AI logics in particular description logics proof theory logic calculi formal verification model theory game theory automata proof search constraint systems model checking and proof construction   Design and Verification of Microprocessor Systems for High-Assurance Applications David S. Hardin,2010-03-02 Microprocessors increasingly control and monitor our most critical systems including automobiles airliners medical systems transportation grids and defense systems The relentless march of semiconductor process technology has given engineers exponentially increasing transistor budgets at constant recurring cost This has encouraged increased functional integration onto a single die as well as increased architectural sophistication of the functional units themselves Additionally design cycle times are decreasing thus putting increased schedule pressure on engineers Not surprisingly this environment has led to a number of uncaught design flaws Traditional simulation based design verification has not kept up with the scale or pace of modern microprocessor system design Formal verification methods offer the promise of improved bug finding capability as well as the ability to establish functional correctness of a detailed design relative to a high level specification However widespread use of formal methods has had to await breakthroughs in automated reasoning integration with engineering design languages and processes scalability and usability This book presents several breakthrough design and verification techniques that allow these powerful formal methods to be employed

in the real world of high assurance microprocessor system design   Automated Technology for Verification and Analysis Bernd Finkbeiner,Geguang Pu,Lijun Zhang,2015-10-07 This book constitutes the proceedings of the 13th International Symposium on Automated Technology for Verification and Analysis ATVA 2015 held in Shanghai China in October 2015 The 27 revised papers presented together with 6 tool papers in this volume were carefully reviewed and selected from 95 submissions They show current research on theoretical and practical aspects of automated analysis verification and synthesis by providing an international forum for interaction among the researchers in academia and industry   **Computer-Aided Reasoning** Matt Kaufmann,Panagiotis Manolios,J Strother Moore,2013-04-17 Computer Aided Reasoning ACL2 Case Studies illustrates how the computer aided reasoning system ACL2 can be used in productive and innovative ways to design build and maintain hardware and software systems Included here are technical papers written by twenty one contributors that report on self contained case studies some of which are sanitized industrial projects The papers deal with a wide variety of ideas including floating point arithmetic microprocessor simulation model checking symbolic trajectory evaluation compilation proof checking real analysis and several others Computer Aided Reasoning ACL2 Case Studies is meant for two audiences those looking for innovative ways to design build and maintain hardware and software systems faster and more reliably and those wishing to learn how to do this The former audience includes project managers and students in survey oriented courses The latter audience includes students and professionals pursuing rigorous approaches to hardware and software engineering or formal methods Computer Aided Reasoning ACL2 Case Studies can be used in graduate and upper division undergraduate courses on Software Engineering Formal Methods Hardware Design Theory of Computation Artificial Intelligence and Automated Reasoning The book is divided into two parts Part I begins with a discussion of the effort involved in using ACL2 It also contains a brief introduction to the ACL2 logic and its mechanization which is intended to give the reader sufficient background to read the case studies A more thorough textbook introduction to ACL2 may be found in the companion book Computer Aided Reasoning An Approach The heart of the book is Part II where the case studies are presented The case studies contain exercises whose solutions are on the Web In addition the complete ACL2 scripts necessary to formalize the models and prove all the properties discussed are on the Web For example when we say that one of the case studies formalizes a floating point multiplier and proves it correct we mean that not only can you read an English description of the model and how it was proved correct but you can obtain the entire formal content of the project and replay the proofs if you wish with your copy of ACL2 ACL2 may be obtained from its home page The results reported in each case study as ACL2 input scripts as well as exercise solutions for both books are available from this page   *Collaboration between Human and Artificial Societies* Julian A. Padget,2006-12-31 The full title of the HCM network project behind this volume is VIM A virtual multicomputer for symbolic applications The three strands which bound the network together were parallel systems advanced compilation techniques andarti cialintelligence witha commonsubstrate in the

programminglanguage Lisp The initial aim of the project was to demonstrate how the combination of these three technologies could be used to build a virtual multicomputer an ephemeral persistent machine of available heterogeneous computing resources for large scale symbolic applications The system would support a virtual processor abstraction to distribute data and tasks across the multicomputer the actual physical composition of which may change dynamically Our practical objective was to assist in the prototyping of dynamic distributed symbolic app cations in arti cial intelligence using whatever resources are available probably networked workstations so that the developed program could also be run on more exotic hardware without reprogramming What we had not foreseen at the outset of the project was how agents would unify the strands at the application level as distinct from the system level o lined above It was as a result of the agent in uence that we held two workshops in May and December 1997 with the title Collaboration between human and arti cial societies The papers collected in this volume are a selection from presentations made at those two workshops In each case the format consisted of a number of invited speakers plus presentations from the network partners **Towards Verified Systems** J. Bowen,2013-10-22 As the complexity of embedded computer controlled systems increases the present industrial practice for their development gives cause for concern especially for safety critical applications where human lives are at stake The use of software in such systems has increased enormously in the last decade Formal methods based on firm mathematical foundations provide one means to help with reducing the risk of introducing errors during specification and development There is currently much interest in both academic and industrial circles concerning the issues involved but the techniques still need further investigation and promulgation to make their widespread use a reality This book presents results of research into techniques to aid the formal verification of mixed hardware software systems Aspects of system specification and verification from requirements down to the underlying hardware are addressed with particular regard to real time issues The work presented is largely based around the Occam programming language and Transputer microprocessor paradigm The HOL theorem prover based on higher order logic has mainly been used in the application of machine checked proofs The book describes research work undertaken on the collaborative UK DTI SERC funded Information Engineering Dictorate Safemos project The partners were Inmos Ltd Cambridge SRI the Oxford University Computing Laboratory and the University of Cambridge Computer Laboratory who investigated the problems of formally verifying embedded systems The most important results of the project are presented in the form of a series of interrelated chapters by project members and associated personnel In addition overviews of two other ventures with similar objectives are included as appendices The material in this book is intended for computing science researchers and advanced industrial practitioners interested in the application of formal methods to real time safety critical systems at all levels of abstraction from requirements to hardware In addition material of a more general nature is presented which may be of interest to managers in charge of projects applying formal methods especially for safety critical systems and others who are considering their use A Computational

<u>Logic Handbook</u> Robert S. Boyer,J Strother Moore,1998 This book provides the definitive documentation for one of the most well known and highly regarded theorem proving programs ever written The program described is one of the more significant enduring and prize awarded accomplishments in the fields of artificial intelligence formal methods and applied logic The book provides an exact statement of the logic for which the program is a prover a complete description of the user s commands installation instructions and much tutorial information including references to thousands of pages of examples Among the examples is a formally verified microprocessor and a formally verified compiler targeting that microprocessor The second edition of A Computational Logic handbook provides all the information necessry for using the most recently releases version of Nqthm the freely availableBoyer Mooretheorem proving program The second edition includes a precise description of all recent changes to the logic in the past nine years including many enhanced syntactic features and rules of inference which were added to support work on large scale projects in formal methods Thousands of pages of fascinating exemplary mathematically checked input are described examples that deal with very difficult questions in formal mehtods and mathematics New material includes Description of the new syntax including COND CASE LET LIST and backquote describes some higher order inference procedures includingconstrained functionsandfunctional instantiation documents more sophisticated control machinery for manipulating very large theories introduces a secure proof checking environment describes thousands of pages of fascinating example input dealing with very difficult questions in formal methods and mathematics provides a formal parserfor the syntax compares the proof complexity of many interesting checked examples includes much new tutorial help especially for the many new features A computational logic is a mathematical logic that is both oriented towards discussion of computation and mechanised so that proofs can be checked by computation The computational logic discussed in the handbook is that developed by Boyer Moore The first edition published in 1988 is an acknowledged classic in the field of formal methods and computational logic However it no longer reflects existing technology The second edition provides a complete overview of the Boyer Moore theorem proving approach Nqthm and provides examples It includes several significant new features that have been aded to the Nquthm system since 1988 The book is structured in thefollowing way Part 1 discusses logic without regard for its mechanisation and answers the question what are the axioms and rules of inference Part 2 discusses its mechanisation and answers the question how does one use the Boyer Moore theorem prover to prove theorems   <u>Journal of the ACM.</u> ,1996   **Trustworthy Translation for the Requirements State Machine Language Without Events** Michael William Whalen,2005   **Programming Languages and Systems** ,2000   *Computer-aided Verification* ,2001   **Proceedings of the ASP-DAC ... Asia and South Pacific Design Automation Conference** ,1995   **Abstract State Machines** ,2000   **Automated Proofs of Object Code for a Widely Used Microprocessor** Yuan Yu,1993 Based on this formal model we have mechanized a mathematical theory to facilitate automated reasoning about object code programs We then have mechanically checked the correctness of MC68020

object code programs for binary search Hoare s Quick Sort the Berkeley Unix C string library and other well known algorithms The object code for these examples was generated using the Gnu C the Verdix Ada and the AKCL Common Lisp compilers   A Simple Formalization and Proof for the Mutilated Chess Board Lawrence C. Paulson,University of Cambridge. Computer Laboratory,1996 Abstract The impossibility of tiling the mutilated chess board has been formalized and verified using Isabelle The formalization is concise because it is expressed using inductive definitions The proofs are straightforward except for some lemmas concerning finite cardinalities This exercise is an object lesson in choosing a good formalization   **Dependable Computing for Critical Applications 7** Charles B. Weinstock,John Rushby,1999 Annotation Presenting all 20 of the conferences talks covers assessing and coping with commercial off the shelf components formal methods distributed systems time triggered architecture fault tolerance and safety models of partitioning for integrated modular avionics dependability evaluation and probabilistic guarantees A summary is also provided for a panel on certifying and assessing critical systems Among the specific topics are building fault tolerant hardware clocks from commercial components improving the performance of atomic broadcast protocols using the newsmonger technique the experimentally validating high speed systems using physical fault injection and evaluating dependability using a multi criteria decision analysis procedure No mention is made of where or when the conference was held There is no subject index Annotation copyrighted by Book News Inc Portland OR   **COMPASS ...** ,1990   **The SECD Microprocessor** Brian T. Graham,1992-05-31 Very Good No Highlights or Markup all pages are intact   **ACM SIGPLAN Notices** ,2006

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- Integration of Multimedia Elements
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