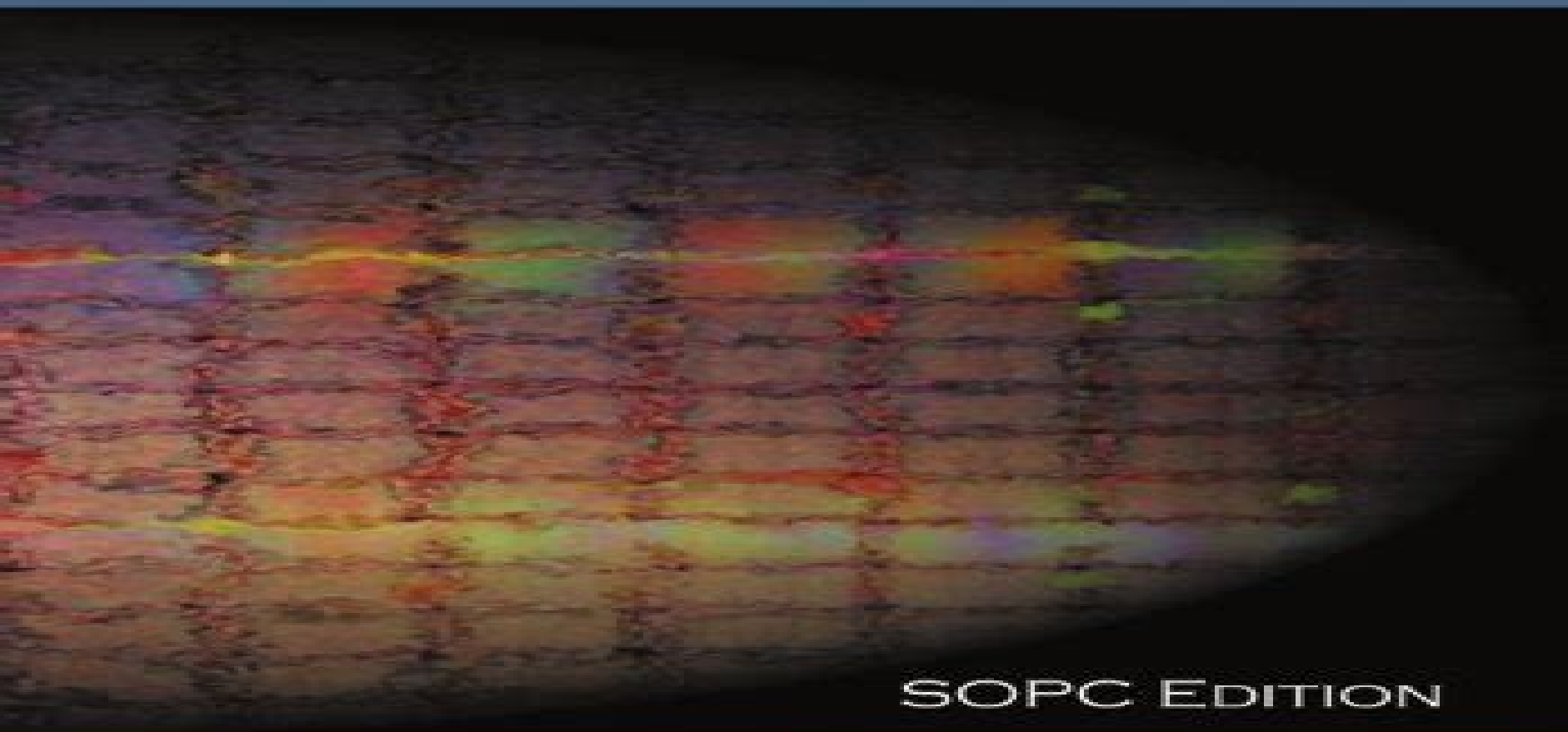


RAPID PROTOTYPING OF DIGITAL SYSTEMS



SOPC EDITION



BY J. O. HAMBLÉN, T. S. HALL, AND M. D. FURMAN

Rapid Prototyping Of Digital Systems

Zoran Salcic



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Rapid Prototyping of Digital Systems James O. Hamblen, Tyson S. Hall, Michael D. Furman, 2007-10-31 Here is a laboratory workbook filled with interesting and challenging projects for digital logic design and embedded systems classes The workbook introduces you to fully integrated modern CAD tools logic simulation logic synthesis using hardware description languages design hierarchy current generation field programmable gate array technology and SoPC design Projects cover such areas as serial communications state machines with video output video games and graphics robotics pipelined RISC processor cores and designing computer systems using a commercial processor core

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Rapid Prototyping of Digital Systems James O. Hamblen, Michael D. Furman, 2004-06-30 Rapid Prototyping of Digital Systems Second Edition provides an exciting and challenging laboratory component for an undergraduate digital logic design class The more advanced topics and exercises are also appropriate for consideration at schools that have an upper level course in digital logic or programmable logic Design engineers working in industry will also want to consider this book for a rapid introduction to FPLD technology and logic synthesis using commercial CAD tools especially if they have not had previous experience with the new and rapidly evolving technology Two tutorials on the Altera CAD tool environment an overview of programmable logic and a design library with several easy to use input and output functions were developed for this book to help the reader get started quickly Early design examples use schematic capture and library components VHDL is used for more complex designs after a short introduction to VHDL based synthesis A coupon is included with the text for purchase of the new UP 1X board The additional logic and memory in the UP 1X s FLEX 10K70 is useful on larger design projects such as computers and video games The second edition includes an update chapter on programmable logic new robot sensors and projects optional Verilog examples and a meta assembler which can be used to develop assemble language programs for the computer designs in Chapters 8 and 13

Rapid Prototyping of Digital Systems James O. Hamblen, Michael D. Furman, 1999-09-30 Rapid Prototyping of Digital Systems provides an exciting and challenging laboratory component for undergraduate digital logic and computer design courses The more advanced topics and exercises also make this text useful for upper level courses in digital logic or programmable logic Design engineers working in industry will want to consider this text for a rapid introduction to PPLD technology and logic synthesis using commercial CAD tools Rapid Prototyping of Digital Systems includes two tutorials on the Altera CAD tool environment an overview of programmable logic and a design library with several easy to use input and output functions These features were developed to help students get started quickly Early design examples use schematic capture and library components VHDL is used for more complex designs after a short introduction to VHDL based synthesis

The approach used in this text reflects contemporary practice in industry more accurately than the more traditional TTL protoboard based laboratory courses. Designs containing up to twenty thousand gates are possible with the Altera Student Version CAD tools and the UP 1 board. Rapid Prototyping of Digital Systems contains a number of interesting and challenging laboratory projects involving serial communications state machines with video output video games and graphics simple computers keyboard and mouse interfaces robotics and a RISC processor core. These projects were all developed on the student version of the Altera CAD tools and can be implemented on the Altera UP 1 board.

Digital Systems Design and Prototyping Using Field Programmable Logic Zoran Salcic, Asim Smailagic, 2014-01-15

Digital Systems James O. Hamblen, Tyson S. Hall, Michael D. Furman, 2006-01-16

Rapid Prototyping of Digital Systems Quartus II Edition provides an exciting and challenging laboratory component for undergraduate digital logic and computer design courses using FPGAs and CAD tools for simulation and hardware implementation. The more advanced topics and exercises also make this text useful for upper level courses in digital logic programmable logic and embedded systems. This new version of the widely used Rapid Prototyping of Digital Systems Second Edition now uses Altera's new Quartus II CAD tool and includes laboratory projects for Altera's UP 2 and the new UP 3 FPGA board. Rapid Prototyping of Digital Systems Quartus II Edition includes four tutorials on the Altera Quartus II and NIOS II tool environment an overview of programmable logic and IP cores with several easy to use input and output functions. These features were developed to help students get started quickly. Early design examples use schematic capture and IP cores developed for the Altera UP FPGA boards. VHDL is used for more complex designs after a short introduction to VHDL based synthesis. New to this edition is an overview of System on a Programmable Chip SOPC technology and SOPC design examples for the UP3 using Altera's new NIOS II Processor hardware and C software development tools.

Digital Systems Design and Prototyping Zoran Salcic, Asim Smailagic, 2000-10-31

A book CD ROM covering digital systems design using two important technologies field programmable logic devices FPLDs and hardware description languages HDLs. These two technologies are combined to aid in the design prototyping and implementation of a range of digital systems. Presents VHDL and Verilog widely used standard languages and the proprietary Altera HDL. Chapters on these languages serve as tutorials and comparisons are made to highlight strengths and weaknesses of each language. The CD ROM contains the Altera MAX PLUS II development environment. Can be used as a reference or an advanced level text. Salcic is affiliated with the University of Auckland. Smailagic is affiliated with Carnegie Mellon University. Annotation copyrighted by Book News Inc. Portland OR.

Rapid Prototyping of Digital Systems Jamaludin Omar, 1997

The development and implementation of a framework for the rapid prototyping of digital systems using programmable hardware is presented. The framework components developed consist of PC cards with programmable hardware components i.e. FPGAs. PCB templates of the PC cards hardware modules for interfacing with PC systems software subroutines appropriate for hardware modules software utility programs for configuring the FPGAs on the PC cards and

examples of designs which include hardware and software The framework allows a digital system to be developed rapidly and tested at realistic clock rates on the PC platform Hardware software and utilisation aspects of the rapid framework are described in detail The framework is shown to be very useful in rapid prototyping of a digital system A coprocessor card for computed tomography application was developed as an application using the rapid prototyping framework With limited capacity static RAM on board it was able to speed up implementation of the filtered backprojection algorithm up to 5 times

VHDL and FPLDs in Digital Systems Design, Prototyping and Customization Zoran Salcic, 2012-12-06 This book represents an attempt to treat three aspects of digital systems design prototyping and customization in an integrated manner using two major technologies VHSIC Hardware Description Language VHDL as a modeling and specification tool and Field Programmable Logic Devices FPLDs as an implementation technology They together make a very powerful combination for complex digital systems rapid design and prototyping as the important steps towards manufacturing or in the case of feasible quantities they also provide fast system manufacturing Combining these two technologies makes possible implementation of very complex digital systems at the desk VHDL has become a standard tool to capture features of digital systems in a form of behavioral dataflow or structural models providing a high degree of flexibility When augmented by a good simulator VHDL enables extensive verification of features of the system under design reducing uncertainties at the latter phases of design process As such it becomes an unavoidable modeling tool to model digital systems at various levels of abstraction

Specification and Design Methodology for Real-Time Embedded Systems Randall S. Janka, 2012-12-06 Specification and design methodology has seen significant growth as a research area over the last decade tracking but lagging behind VLSI design technology in general and the CAD industry in particular The commercial rush to market tries to leverage existing technology which fuels CAD design tool development Paralleling this is very active basic and applied research to investigate and move forward rational and effective methodologies for accomplishing digital design especially in the field of hardware software codesign It is this close relationship between industry and academia that makes close cooperation between researchers and practitioners so important and monographs like this that combine both abstract concept and pragmatic implementation deftly bridge this often gaping chasm It was at the IEEE ACM Eighth International Symposium on Hardware Software Codesign where I met the author of this monograph Dr Randall Janka who was presenting some of his recent dissertation research results on specification and design methodology or as he has so succinctly defined this sometimes ambiguous concept the tools and rules Where so many codesign researchers are trying to prove out different aspects of codesign and using toy applications to do so Dr Janka had developed a complete specification and design methodology and prototyped the infrastructure and proven its viability utility and effectiveness using a demanding real world application of a real time synthetic aperture radar imaging processor that was implemented with embedded parallel processors Advanced Digital Systems Design with Rapid Prototyping on FPGAs Using VHDL Lucien Ngalamou, 2012-08-01 Advanced Digital

Systems Design with Rapid Prototyping on FPGAs using VHDL aims to provide students researcher and hardware designers in electrical computer engineering with a reference manual that covers the main aspects of hardware implementation of complex algorithms in the field of digital technology using FPGAs

CONTROL SYSTEMS, ROBOTICS AND AUTOMATION - Volume IV Heinz Unbehauen, 2009-10-11 This Encyclopedia of Control Systems Robotics and Automation is a component of the global Encyclopedia of Life Support Systems EOLSS which is an integrated compendium of twenty one Encyclopedias This 22 volume set contains 240 chapters each of size 5000 30000 words with perspectives applications and extensive illustrations It is the only publication of its kind carrying state of the art knowledge in the fields of Control Systems Robotics and Automation and is aimed by virtue of the several applications at the following five major target audiences University and College Students Educators Professional Practitioners Research Personnel and Policy Analysts Managers and Decision Makers and NGOs

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