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Handbook of Semiconductor Manufacturing Technology Yoshio Nishi, Robert Doering, 2017-12-19 Retaining the comprehensive and in depth approach that cemented the bestselling first edition's place as a standard reference in the field the Handbook of Semiconductor Manufacturing Technology Second Edition features new and updated material that keeps it at the vanguard of today's most dynamic and rapidly growing field Iconic experts Robert Doering and Yoshio Nishi have again assembled a team of the world's leading specialists in every area of semiconductor manufacturing to provide the most reliable authoritative and industry leading information available Stay Current with the Latest Technologies In addition to updates to nearly every existing chapter this edition features five entirely new contributions on Silicon on insulator SOI materials and devices Supercritical CO₂ in semiconductor cleaning Low dielectrics Atomic layer deposition Damascene copper electroplating Effects of terrestrial radiation on integrated circuits ICs Reflecting rapid progress in many areas several chapters were heavily revised and updated and in some cases rewritten to reflect rapid advances in such areas as interconnect technologies gate dielectrics photomask fabrication IC packaging and 300 mm wafer fabrication While no book can be up to the minute with the advances in the semiconductor field the Handbook of Semiconductor Manufacturing Technology keeps the most important data methods tools and techniques close at hand

Prozeßtechnologie Günter Schumicki, Peter Seegebrecht, 2013-04-18 Prozeßtechnologie behandelt Fertigungsverfahren integrierter Schaltungen in CMOS Technologie Hierbei werden zunächst die Einzelprozesse behandelt die der Strukturerzeugung Strukturübertragung Schichtenherzeugung und Schichtmodifikation zugerechnet werden Im Anschluß daran wird gezeigt wie Einzelprozesse zu einem Gesamtprozeß für die Herstellung von CMOS Schaltungen integriert werden Auf die Charakterisierung der Prozesse und die Entwicklung der Entwurfsregeln wird eingegangen Das Buch basiert auf dem reichen Erfahrungsschatz fertigungsorientierter Technologen

Applied Mechanics Reviews, 1985 **Low Temperature Epitaxial Growth of Semiconductors** Takashi Hariu, 1991 Low temperature processes for semiconductors have been recently under intensive development to fabricate controlled device structures with minute dimensions in order to achieve the highest device performance and new device functions as well as high integration density Comprising reviews by experts long involved in the respective pioneering work this volume makes a useful contribution toward maturing the process of low temperature epitaxy as a whole

Nanofabrication Zheng Cui, 2024-07-13 Nanofabrication Principles Capabilities and Limits provides a practical guide to nanofabrication technologies and processes It was first published in 2008 and is now in an updated third edition The book introduces readers to the fundamentals and recent developments in nanofabrication techniques with chapters covering optical lithography electron beam lithography and nanoimprinting lithography as well as nanofabrication by focused ion beams scanning tips self assembly and nanoscale pattern transfer by etching and deposition There is also a chapter describing various tricks that enable the fabrication of nanostructures that would otherwise be impossible using traditional

methods The unique feature of this book is that each technique introduced is not only about its capabilities but also its limits so that the readers are fully aware of the best options to choose from a toolbox of nanofabrication processes covered in the book

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