

# Example: VLSI Floorplanning with Simulated Annealing

Modules are displayed as yellow rectangles; terminals are marked by small black rectangles. Connections between modules are displayed using red lines. This display of nets is sometimes known as a "rat's nest" diagram.

On the left, it displays a diagram of a floorplan and its estimated cost.



On the right, it displays annealing status information, controls, and a plot of maximum (red), average (black), and minimum (green) accepted configuration costs at each temperature.

<http://foghorn.cadlab.lafayette.edu/cadapplets/fp/fpApplet.html>

# Simulated Annealing For Vlsi Design

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## **Simulated Annealing For Vlsi Design:**

*Simulated Annealing for VLSI Design* D.F. Wong,H.W. Leong,H.W. Liu,2012-12-06 This monograph represents a summary of our work in the last two years in applying the method of simulated annealing to the solution of problems that arise in the physical design of VLSI circuits Our study is experimental in nature in that we are concerned with issues such as solution representations neighborhood structures cost functions approximation schemes and so on in order to obtain good design results in a reasonable amount of computation time We hope that our experiences with the techniques we employed some of which indeed bear certain similarities for different problems could be useful as hints and guides for other researchers in applying the method to the solution of other problems Work reported in this monograph was partially supported by the National Science Foundation under grant MIP 87 03273 by the Semiconductor Research Corporation under contract 87 DP 109 by a grant from the General Electric Company and by a grant from the Sandia Laboratories     Simulated Annealing for VLSI Design D F Wong,H W Leong,H W Liu,1988-03-31     *Hardware Design and Simulation in VAL/VHDL* Larry M. Augustin,David C. Luckham,Benoit A. Gennart,Youm Huh,A. Stanculescu,2012-12-06 The VHSIC Hardware Description Language VHDL provides a standard machine processable notation for describing hardware VHDL is the result of a collaborative effort between IBM Intermetrics and Texas Instruments sponsored by the Very High Speed Integrated Circuits VHSIC program office of the Department of Defense beginning in 1981 Today it is an IEEE standard 1076 1987 and several simulators and other automated support tools for it are available commercially By providing a standard notation for describing hardware especially in the early stages of the hardware design process VHDL is expected to reduce both the time lag and the cost involved in building new systems and upgrading existing ones VHDL is the result of an evolutionary approach to language development starting with high level hardware description languages existing in 1981 It has a decidedly programming language flavor resulting both from the orientation of hardware languages of that time and from a major requirement that VHDL use Ada constructs wherever appropriate During the 1980 s there has been an increasing current of research into high level specification languages for systems particularly in the software area and new methods of utilizing specifications in systems development This activity is worldwide and includes for example object oriented design various rigorous development methods mathematical verification and synthesis from high level specifications VAL VHDL Annotation Language is a simple further step in the evolution of hardware description languages in the direction of applying new methods that have developed since VHDL was designed     *Steady-State Methods for Simulating Analog and Microwave Circuits* Kenneth S. Kundert,Jacob K. White,Alberto L. Sangiovanni-Vincentelli,2013-03-09 The motivation for starting the work described in this book was the interest that Hewlett Packard s microwave circuit designers had in simulation techniques that could tackle the problem of finding steady state solutions for nonlinear circuits particularly circuits containing distributed elements such as transmission lines Examining the problem of computing steady state solutions in this

context has led to a collection of novel numerical algorithms which we have gathered along with some background material into this book. Although we wished to appeal to as broad an audience as possible to treat the subject in depth required maintaining a narrow focus. Our compromise was to assume that the reader is familiar with basic numerical methods such as might be found in Dahlquist<sup>74</sup> or Vlach<sup>83</sup> but not assume any specialized knowledge of methods for steady state problems. Although we focus on algorithms for computing steady state solutions of analog and microwave circuits the methods herein are general in nature and may find use in other disciplines. A number of new algorithms are presented the contributions primarily centering around new approaches to harmonic balance and mixed frequency time methods. These methods are described along with appropriate background material in what we hope is a reasonably satisfying blend of theory practice and results. The theory is given so that the algorithms can be fully understood and their correctness established.

**ASIC System Design with VHDL: A Paradigm** Steven S. Leung, Michael A. Shanblatt, 1989-06-30 Beginning in the mid 1980's VLSI technology had begun to advance in two directions. Pushing the limit of integration ULSI Ultra Large Scale Integration represents the frontier of the semiconductor processing technology in the campaign to conquer the submicron realm. The application of ULSI however is at present largely confined in the area of memory designs and as such its impact on traditional microprocessor based system design is modest. If advancement in this direction is merely a natural extrapolation from the previous integration generations then the rise of ASIC Application Specific Integrated Circuit is an unequivocal signal that a directional change in the discipline of system design is in effect. In contrast to ULSI ASIC employs only well proven technology and hence is usually at least one generation behind the most advanced processing technology. In spite of this apparent disadvantage ASIC has become the mainstream of VLSI design and the technology base of numerous entrepreneurial opportunities ranging from PC clones to supercomputers. Unlike ULSI whose complexity can be hidden inside a memory chip or a standard component and thus can be accommodated by traditional system design methods ASIC requires system designers to master a much larger body of knowledge spanning from processing technology and circuit techniques to architecture principles and algorithm characteristics. Integrating knowledge in these various areas has become the precondition for integrating devices and functions into an ASIC chip in a market oriented environment. But knowledge is of two kinds.

*Mixed-Mode Simulation* Resve A. Saleh, A. Richard Newton, 2012-12-06 Our purpose in writing this book was two fold. First we wanted to compile a chronology of the research in the field of mixed mode simulation over the last ten to fifteen years. A substantial amount of work was done during this period of time but most of it was published in archival form in Masters theses and Ph D dissertations. Since the interest in mixed mode simulation is growing and a thorough review of the state of the art in the area was not readily available we thought it appropriate to publish the information in the form of a book. Secondly we wanted to provide enough information to the reader so that a proto type mixed mode simulator could be developed using the algorithms in this book. The SPLICE family of programs is based on the algorithms and techniques

described in this book and so it can also serve as documentation for these programs

**ACKNOWLEDGEMENTS** The authors would like to dedicate this book to Prof D O Pederson for inspiring this research work and for providing many years of support and encouragement The authors enjoyed many fruitful discussions and collaborations with Jim Kleckner Young Kim Alberto Sangiovanni Vincentelli and Jacob White and we thank them for their contributions We also thank the countless others who participated in the research work and read early versions of this book Lillian Beck provided many useful suggestions to improve the manuscript Yun cheng Ju did the artwork for the illustrations

**Algorithms and Theory of Computation Handbook** Mikhail J. Atallah, 1998-11-23 Algorithms and Theory of Computation Handbook is a comprehensive collection of algorithms and data structures that also covers many theoretical issues It offers a balanced perspective that reflects the needs of practitioners including emphasis on applications within discussions on theoretical issues Chapters include information on finite precision issues as well as discussion of specific algorithms where algorithmic techniques are of special importance including graph drawing robotics forming a VLSI chip vision and image processing data compression and cryptography The book also presents some advanced topics in combinatorial optimization and parallel distributed computing applications areas where algorithms and data structuring techniques are of special importance graph drawing robot algorithms VLSI layout vision and image processing algorithms scheduling electronic cash data compression dynamic graph algorithms on line algorithms multidimensional data structures cryptography advanced topics in combinatorial optimization and parallel distributed computing

*Gallium Arsenide Digital Circuits* Omar Wing, 2012-12-06 Gallium Arsenide technology has come of age GaAs integrated circuits are available today as gate arrays with an operating speed in excess of one Gigabits per second Special purpose GaAs circuits are used in optical fiber digital communications systems for the purpose of regeneration multiplexing and switching of the optical signals As advances in fabrication and packaging techniques are made the operating speed will further increase and the cost of production will reach a point where large scale application of GaAs circuits will be economical in these and other systems where speed is paramount This book is written for students and engineers who wish to enter into this new field of electronics for the first time and who wish to embark on a serious study of the subject of GaAs circuit design No prior knowledge of GaAs technology is assumed though some previous experience with MOS circuit design will be helpful A good part of the book is devoted to circuit analysis to the extent that is possible for non linear circuits The circuit model of the GaAs transistor is derived from first principles and analytic formulas useful in predicting the approximate circuit performance are also derived Computer simulation is used throughout the book to show the expected performance and to study the effects of parameter variations

**Models for Large Integrated Circuits** Patrick DeWilde, Zhen-Qiu Ning, 2012-12-06 A modern microelectronic circuit can be compared to a large construction a large city on a very small area A memory chip a DRAM may have up to 64 million bit locations on a surface of a few square centimeters Each new generation of integrated circuit generations are measured by factors of four in

overall complexity requires a substantial increase in density from the current technology added precision a decrease of the size of geometric features and an increase in the total usable surface The microelectronic industry has set the trend Ultra large funds have been invested in the construction of new plants to produce the ultra large scale circuits with utmost precision under the most severe conditions The decrease in feature size to submicrons 0.7 micron is quickly becoming available does not only bring technological problems New design problems arise as well The elements from which microelectronic circuits are built transistors and interconnects have different shape and behave differently than before Phenomena that could be neglected in a four micron technology such as the non uniformity of the doping profile in a transistor or the mutual capacitance between two wires now play an important role in circuit design This situation does not make the life of the electronic designer easier he has to take many more parasitic effects into account up to the point that his ideal design will not function as originally planned

**Computational Electronics** Karl Hess, J.P. Leburton, U.

Ravaioli, 2013-03-14 Large computational resources are of ever increasing importance for the simulation of semiconductor processes devices and integrated circuits The Workshop on Computational Electronics was intended to be a forum for the discussion of the state of the art of device simulation Three major research areas were covered conventional simulations based on the drift diffusion and the hydrodynamic models Monte Carlo methods and other techniques for the solution of the Boltzmann transport equation and computational approaches to quantum transport which are relevant to novel devices based on quantum interference and resonant tunneling phenomena Our goal was to bring together researchers from various disciplines that contribute to the advancement of device simulation These include Computer Science Electrical Engineering Applied Physics and Applied Mathematics The success of this multidisciplinary formula was proven by numerous interactions which took place at the Workshop and during the following three day Short Course on Computational Electronics The format of the course including a number of tutorial lectures and the large attendance of graduate students stimulated many discussions and has proven to us once more the importance of cross fertilization between the different disciplines

**Metaheuristics in Combinatorial Optimization** Gilbert Laporte, Ibrahim H. Osman, 1996 [CAD/CAM Abstracts](#), 1992

**Emergent Computation** Stephanie Forrest, 1991 Researchers in several fields are exploring computational systems in which interesting global behavior emerges from local interactions among component parts an approach called emergent computation In these systems interactions among simultaneous computations are exploited to improve efficiency increase flexibility or provide more realistic models of natural phenomena These 31 essays define and explore the concept of emergent computation in such areas as artificial networks adaptive systems classifier systems connectionist learning other learning and biological networks to determine what properties are required of the supporting architectures that generate them Many of the essays share the themes of design how to construct such systems the importance of preexisting structure to learning and the role of parallelism and the tension between cooperative and competitive models of interaction In the

introduction Stephanie Forrest presents several detailed examples of the kinds of problems emergent computation can address These include showing how emergent computation can lead to efficiency improvements in parallel processing establishing the connection between emergent computation and nonlinear systems and comparing two search techniques to show how the emergent computational approach to a problem differs from other more conventional approaches Stephanie Forrest is Assistant Professor in the Department of Computer Science at the University of New Mexico She is also affiliated with the Center for Nonlinear Studies and Computing Division at Los Alamos National Laboratory     Iterative Computer Algorithms with Applications in Engineering Sadiq M. Sait,Habib Youssef,1999 The book includes an introduction to fuzzy logic and its application in the formulation of multi objective optimization problems a discussion on hybrid techniques that combine features of heuristics a survey of recent research work and examples that illustrate required mathematical concepts

BOOK JACKET     **VLSI Placement and Global Routing Using Simulated Annealing** Carl Sechen,1988-08-31     1991  
IEEE International Symposium on Circuits and Systems ,1991     **Digest of Technical Papers** ,2001     **Routing Problems in the Physical Design of Integrated Circuits** Hon Wai Leong,1986     **IMACS '91, 13th World Congress on Computation and Applied Mathematics** Robert Vichnevetsky,John J. H. Miller,1991 Proceedings Computer Arithmetic Algebra OOP     Nieuw archief voor wiskunde ,1991

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