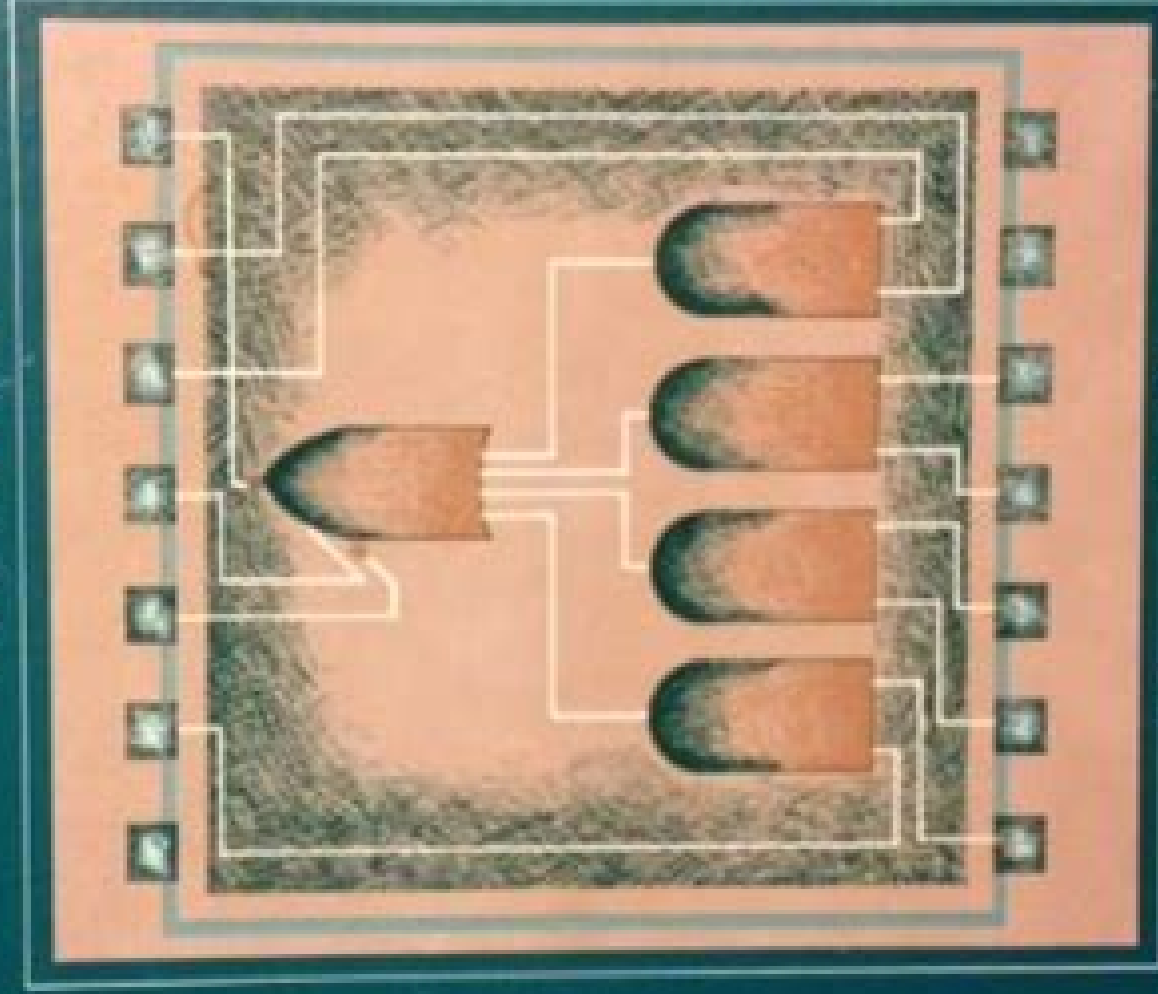


Joseph D. Greenfield

PRACTICAL DIGITAL DESIGN USING ICs



Second Edition

Practical Digital Design Using Ics 3ed

Basil H. Vassos, Galen Wood Ewing



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based on the system performance gains that can be achieved by stacking and vertically interconnecting distinct device chips. The 3 D concept of replacing long 2 D interconnects with shorter vertical 3 D interconnects has the potential to alleviate the well known interconnect RC delay problem facing the semiconductor industry. Additional benefits of the 3 D concept for the IC maker include reduced die size and the ability to use distinct technologies analog logic RF etc on separate vertically interconnected layers. The 3 D concept therefore allows the integration of otherwise incompatible technologies and offers significant advantages in performance functionality and form factor. Topics in this book include fabrication of 3 D ICs modeling simulation and scaling of 3 D integrated devices applications of 3 D integration through wafer interconnects for 3 D packaging and interposer applications bonding technology for 3 D integration and enabling processes for 3 D integration.

Analog and Digital Electronics for Scientists Basil H. Vassos, Galen Wood Ewing, 1980 Subject Guide to Books in Print, 2001 *Digital Theory and Experimentation Using Integrated Circuits* Morris E. Levine, 1982 EMBC 2004 IEEE

Engineering in Medicine and Biology Society. Conference, 2004 **Using Microprocessors and Microcomputers** Joseph D. Greenfield, William C. Wray, 1988. This book covers all Motorola 8 bit microprocessors and introduces the more advanced 16 bit chips. The latest advances in the 6800 family and their growing applications in today's designs are explored and then a concise two chapter treatment of the 16 bit 6800 is given. There is also coverage of the complete line of Motorola peripherals.

Principles and Applications of Digital Electronics Larry D. Jones, 1986 Catalog Southwestern Indian Polytechnic Institute,

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