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ABSTRACT

The field of parallel discrete event simulation is entering a period of self-assessment. Fifteen years of investigation has witnessed great strides in techniques for efficiently executing discrete event simulations on parallel and distributed machines. Still, the discrete event simulation community at large has failed to recognize many of these results. The central question is, why has this occurred? One possible reason is an apparent disagreement in both the focus and objectives of the parallel discrete event simulation research community (primarily computer scientists) and the discrete event simulation community (a widely diverse group including, operations researchers, management scientists, mathematicians, and statisticians, as well as computer scientists). An examination of parallel discrete event simulation from a modeling methodological perspective illustrates some of these differences and reveals potentials for their resolution.

1 INTRODUCTION

In a recent series of articles (Abrams 1993; Bagrodia 1993; Fujimoto 1993a,1993b; Lin 1993; Reynolds 1993; Unger and Cleary 1993) some of the leading researchers in parallel discrete event simulation (PDES) provide their assessment of the field. In the lead article, Fujimoto observes that a central problem for PDES is its failure to "make a significant impact in the general simulation community" during the last 15 years of its development (Fujimoto 1993a, p. 228). Several factors are identified as contributing to this lack of impact, and various approaches are proposed to make parallel simulation "more accessible to the simulation community." The companion articles each identify and bemoan the *acceptance* problem, and while perhaps not reaching a consensus pertaining to the correct resolution of this (and related) problems, a unanimous call is given that discussion and evaluation of the place, role and future of parallel discrete event simulation as a problem solving technique needs to become an important part of PDES literature.

Abrams (1993) suggests that the precipitate cause of the problem is a dichotomy in the perspectives and priorities of modeling methodologists – the primary purveyors of discrete event simulation, and parallel programmers – the leaders in parallel discrete event simulation, noting that until these two camps come to terms

it is unlikely that PDES will thrive. Fujimoto (1993b, p. 247) acknowledges the correctness of this assertion and labels the potential collaborative attack as a "worthy goal."

This paper is a response to that call, and presents a view of the parallel discrete event simulation problem from a modeling methodological perspective. Of course, such a premise borders on self-aggrandizement; let us stipulate that we make no claim to be the standard-bearers of modeling methodology for discrete event simulation. However, the views expressed herein do come from a background of over 25 years of thinking about the fundamental issues of how to do discrete event simulation. Consequently, we hope to provide a somewhat different view of the problem – lending new insights into the current status of PDES, and perhaps shedding some light on the far more important question of where should PDES go from here?

A context for answering this question requires answers to even more fundamental questions. These questions form much of the remainder of the paper.

2 WHAT IS SIMULATION?

First and foremost, the question must be asked, what is a simulation? Any number of definitions can be gleaned from a variety of distinguished texts (see Fishman (1973), Franta (1977), Law and Kelton (1991), Shannon (1975)) but for purposes of this discussion we may regard simulation as:

The use of a mathematical/logical model as an experimental vehicle to answer questions about a referent system.

This definition seems to be efficient in the use of words and careful not to presume certain conditions or implicit purposes. For example, computer simulation is not mandated; the model could follow either discrete event or continuous forms; the answers might not be correct; and the system could exist or be envisioned.

Essentially, a simulation is the basis for making some decision – this decision being based on the "answers" provided by the simulation. The relative importance of the decision, once made, and the subsequent action (or inaction) taken as a result of the decision are myriad. Oftentimes, the simulation provides an assessment of some system which is not readily amenable to other types of analysis, thus the simulation provides the only means by which to assess a given situation. So, the ramifications of making an incorrect simulation-based decision can range from a mere nuisance, to loss of investment, to more catastrophic consequences such as the loss of lives. Accordingly, *arriving at the correct decision is the singular overriding objective of simulation*. One may want a simulation to provide a variety of behaviors and possess a multitude

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Chakraborty,Jörg Eberspächer,2012-02-07 This volume contains the lectures given in honor to Georg F rber as tribute to his contributions in the area of real time and embedded systems The chapters of many leading scientists cover a wide range of aspects like robot or automotive vision systems or medical aspects *IEEE First ICA3PP V. Lakshmi Narasimhan,IEEE*

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optimization problems in a diverse range of industries Each chapter is written by leading experts in the fields of parallel and distributed optimization Collectively the contributions serve as a complete reference to the field of combinatorial optimization including details and findings of recent and ongoing investigations

A Framework for the Simulation of Complex Real-time Systems Matthew Francis Storch, University of Illinois at Urbana-Champaign. Department of Computer Science, 1997

Spectral Logic and Its Applications for the Design of Digital Devices Mark G. Karpovsky, Radomir S. Stankovic, Jaakko T. Astola, 2008-05-27 Spectral techniques facilitate the design and testing of today's increasingly complex digital devices There is heightened interest in spectral techniques for the design of digital devices dictated by ever increasing demands on technology that often cannot be met by classical approaches Spectral methods provide a uniform and consistent theoretic environment for recent achievements in this area which appear divergent in many other approaches Spectral Logic and Its Applications for the Design of Digital Devices gives readers a foundation for further exploration of abstract harmonic analysis over finite groups in the analysis design and testing of digital devices After an introduction this book provides the essential mathematical background for discussing spectral methods It then delves into spectral logic and its applications covering Walsh Haar arithmetic transform Reed Muller transform for binary valued functions and Vilenkin Chrestenson transform generalized Haar and other related transforms for multiple valued functions Polynomial expressions and decision diagram representations for switching and multiple value functions Spectral analysis of Boolean functions Spectral synthesis and optimization of combinational and sequential devices Spectral methods in analysis and synthesis of reliable devices Spectral techniques for testing computer hardware This is the authoritative reference for computer science and engineering professionals and researchers with an interest in spectral methods of representing discrete functions and related applications in the design and testing of digital devices It is also an excellent text for graduate students in courses covering spectral logic and its applications

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