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## 1. Introduction

In this paper we describe an optimizing compiler for Scheme [3, 13] called *Orbit* that incorporates our experience with an earlier Scheme compiler called TC [10, 11], together with some ideas from Steele's Rabbit compiler [14]. The three main design goals have been correctness, generating very efficient compiled code, and portability.

In spirit, Orbit is similar to the Rabbit compiler in that it depends on a translation of source code into "continuation-passing style" (CPS), a convenient intermediate form that makes control-flow explicit. After CPS conversion, procedures take an extra argument called a *continuation* (another procedure) that represents the next logical execution point after execution of the procedure body. Thus procedures do not "return," but rather "continue into" the code represented by the continuation. This permits, for example, a general but simple way to optimize tail-recursions into loops.

Steele's seminal work on Rabbit demonstrated the general benefits of this approach to compiler design. However, his work was primarily research oriented, and Rabbit was essentially a prototype compiler (consider, for example, that it generated MACLISP code). TC, on the other hand, was one of the first practical compilers for a Scheme dialect, and much was learned through its design and construction.<sup>2</sup> Orbit now represents a culmination of that learning process, in which CPS conversion has been implemented thoroughly, extended in critical ways, and set in a framework of other important compiler innovations to yield a practical compiler that generates production-quality code competitive with the best compilers for Lisp as well as non-Lisp languages.<sup>3</sup> The new ideas in Orbit include:

1. An extension to CPS conversion called *assignment conversion* that facilitates treatment of assigned variables. (See section 3.)
2. Compact and efficient runtime data representations, the most important being those for lexical closures, which are represented in different ways depending on information gleaned from a static analysis of their use. (See sections 4 and 5.)
3. A register allocation strategy based on *trace scheduling* that optimizes register usage across forks and joins (and which, because of CPS conversion, includes procedure calls). (See section 6.)
4. An integral table-driven assembler that uses hints from the code generator to order blocks so as to minimize the number and size of jumps. (See section 7.)
5. A technique for defining the behavior of primitive operations in Scheme source modules rather than in the internal logic of the compiler. (See section 9.1.)
6. Flexibility in configuring the runtime support system for user programs. (See section 9.2.)

<sup>1</sup>This work was supported in part by NSF Grants DCR-8403304 and DCR-8451415, and a Faculty Development Award from IBM. In addition, some of the work was supported at DEC Western Research Laboratory in the summer of 1984.

<sup>2</sup>Both TC and the S-1 Common Lisp compiler described in [2] have as a common ancestor a Lisp compiler which Steele wrote during the summer of 1979. From this compiler TC was derived by making modifications for compilation of a different source language for different target machines, and adding the logic necessary for compiling lexical closures moderately well.

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# Proceedings Of The Sigplan 84 Symposium On Compiler Construction Montreal

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