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Silicon Front-End Technology — Materials Processing and Modelling

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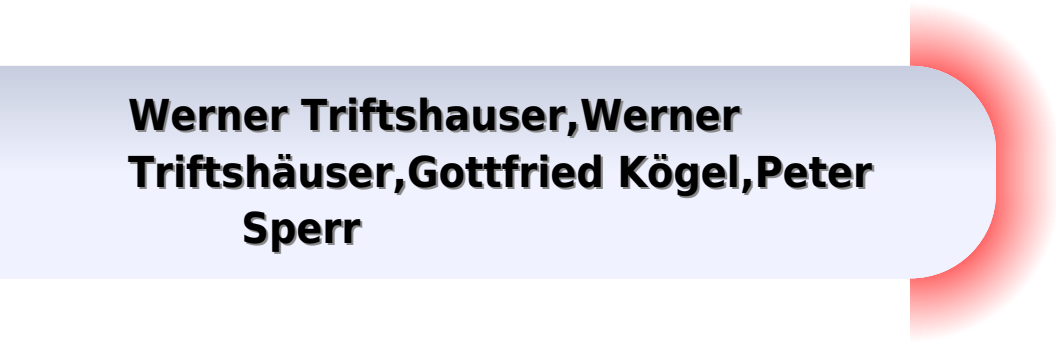
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Simulation of Semiconductor Processes and Devices 2007 Tibor Grasser, 2007-09-18 This volume contains the proceedings of the 12th International Conference on Simulation of Semiconductor Processes and Devices SISPAD 2007 held September 2007 in Vienna Austria It provides a global forum for the presentation and discussion of recent advances and developments in the theoretical description physical modeling and numerical simulation and analysis of semiconductor fabrication processes device operation and system performance

Simulation of Semiconductor Processes and Devices 2001 Dimitris Tsoukalas, Christos Tsamis, 2012-12-06 This volume contains the Proceedings of the International Conference on Simulation of Semiconductor Devices and Processes SISPAD 01 held on September 5 7 2001 in Athens The conference provided an open forum for the presentation of the latest results and trends in process and device simulation The trend towards shrinking device dimensions and increasing complexity in process technology demands the continuous development of advanced models describing basic physical phenomena involved New simulation tools are developed to complete the hierarchy in the Technology Computer Aided Design simulation chain between microscopic and macroscopic approaches The conference program featured 8 invited papers 60 papers for oral presentation and 34 papers for poster presentation selected from a total of 165 abstracts from 30 countries around the world These papers disclose new and interesting concepts for simulating processes and devices

Proceedings of the Fifth International Symposium of Process Physics and Modeling in Semiconductor Technology C. S. Murthy, G. R. Srinivasan, Scott T. Dunham, 1999

Reinigung und Gasphasenepitaxie in einem Ultrahochvakuum-Mehrkammersystem für zukünftige CMOS-Technologien Anton Bayerstadler, 2006-09-14 Die Arbeit beschäftigt sich mit der Reinigung und Gasphasenepitaxie in einem Ultrahochvakuum Mehrkammersystem UHV Mehrkammersystem für zukünftige CMOS Technologien Der Schwerpunkt der Arbeit liegt dabei in der Entwicklung von Prozessen für das Kontakt Engineering von Source und Drain S D für sub 100 nm MOSFETs Mögliche Anwendungen liegen in der Implementierung von erhöhten S D Kontakten zur Erniedrigung der Source Drain Anschlusswiderstände oder dem epitaktischen Auffüllen von S D Extension Gebieten zur Minimierung von Kurzkanaleffekten Im Rahmen dieser Arbeit wurde zum ersten Mal in einem Clustertool eine Wasserstoff Plasmareinigung zur in situ LT Reinigung von planaren oder vorstrukturierten Substraten mit UHV CVD Epitaxie kombiniert Die Verwendung eines SiH₄ GeH₄ Prekursorengemisches erlaubt es bei den typischen Prozessdrücken von 10⁻⁴ mbar bis 10⁻² mbar auch ohne Chlorchemie selektive Abscheidung von Silizium und Silizium Germanium Legierungen zu gewährleisten Insgesamt lässt sich damit ein Gesamtprozess inklusive Vorreinigung und selektiver Abscheidung SEG bei Temperaturen T 800 °C für die Si UHV CVD und T 600 °C für die SiGe UHV CVD etablieren Die technologischen Rahmenbedingungen welche in der Entwicklung eines selektiven LT Epitaxieprozesses ohne chlorierte Prekursoren in Kombination mit einer in situ LT Reinigung in einem Clustertool gegeben sind wurden damit vollständig erfüllt Reinigung Es wurden grundlegende Untersuchungen von verschiedenen

UHV kompatiblen Reinigungsverfahren die sich durch unterschiedliche Temperaturbereiche und die Reinigungseffizienz unterscheiden durchgef hrt Ergebnisse aus Untersuchungen zur thermischen Desorption im UHV HT Reinigung konnten dabei auf die Wasserstoff Plasmareinigung LT Reinigung und die thermische Desorption in reaktiven Gasen LT Reinigung umgesetzt werden Das Hauptaugenmerk lag jeweils im Nachweis von Sauerstoff Kohlenstoff und Stickstoff Verunreinigungen Es zeigt sich dass die thermische Desorption bei technologisch relevanten Temperaturen im UHV equivalent zur thermischen Desorption in Inertgasumgebungen z B dem sog H₂ bake ist Die H₂ Plasmareinigung ist mit einer maximalen Prozesstemperatur von ca 225 C eine LT Reinigung Es konnte erstmals nachgewiesen werden dass die tzwirkung des Wasserstoffplasmas auf Oxid durch die Sputterwirkung von Ar Ionen initiiert wird Durch Ionenbeschuss wird das Oxid gesch digt und die obersten Si O Bindungen werden aufgebrochen Durch die H Radikale erfolgt schlie lich ein ganzfl chiger Oxidabtrag Gasphasenepitaxie Untersuchungen zur Morphologie von SiGe Schichten haben gezeigt dass sich im Gegensatz zur thermischen Desorption bei 900 C mit H₂ Plasmareinigung als Vorreinigung schon bei 500 C mittels UHV CVD glatte SiGe Schichten herstellen lassen Aufbauend auf den Ergebnissen zur ganzfl chigen Epitaxie von Si und SiGe mittels UHVCVD wurde die Selektivit t bez glich vorstrukturierter Oxid und Nitridsubstrate untersucht Die Inkubationszeit f r das Poly Wachstum auf Nitrid ist sehr viel geringer als f r die Poly Nukleation auf Oxid Eine selektive Abscheidung von Si in Si Fenstern von vorstrukturierten Nitridsubstraten ist f r UHV CVD bei 800 C und thermischer Desorption als Vorreinigung daher nicht m glich Weitere Untersuchungen haben gezeigt dass die Inkubationszeit vom Silan bzw Germanpartialdruck abh ngt Zus tzlich wird das Nukleationsverhalten bei UHV CVD und H₂ Plasmareinigung als Vorreinigung durch die Plasmaparameter z B der Plasmadauer bestimmt Prozesstechnisch kommt es bei Abscheidung im massentransportabh ngigen Temperaturbereich bei Si UHV CVD z B bei 800 C unabh ngig von der Vorreinigung zu bekannten Effekten wie dem chemischen Loading Effekt und dem Facettenwachstum von Si 111 und Si 311 Mesafl chen Beide Effekte sind prinzipiell unerw nscht Zur elektrischen Charakterisierung der Kristallqualit t von selektiv gewachsenen i Si Schichten wurden SEG Mesen im Si Fenster eines vorstrukturierten Oxidsubstrats gewachsen und zu einer nip Diode prozessiert Die bestimmte Volumendefektdichte entspricht der Dichte von ganzfl chig abgeschiedenen intrinsischen Siliziumschichten Glatte SiGe Schichten mit einer ausreichenden maximalen selektiven Schichtdicke von ca 30 nm konnten schlie lich mit SiGe UHV CVD bei 500 C in Kombination mit der H₂ Plasmareinigung als Vorreinigung hergestellt werden Damit steht ein UHV kompatibler Prozess zur Herstellung von vertikal erh hten Source Drain Gebieten in planaren MOSFETs zur Verf gung LP CVD ist f r die Abscheidung selektiver SiGe Schichten nicht geeignet weshalb bei industriellen RP CVD Prozessen SEG nur mit chlorierten Prekursoren bzw durch Beimischung von HCl als tsgas funktioniert Die Anwendung des Prozesses zur Herstellung eines epitaktischen Source Drain Anschlussgebietes eines MOSFETs der 90 nm Technologie wurde ebenfalls versucht Das tzen der S D Extension erfolgt durch ein sequentielles H₂ Plasma tzen des Oxids mit anschlie endem Si tzen Das Wiederauff llen der

freigezogene Extensionsgebiete ist aufgrund der gezeigten Selektivität zum Spacer Material möglich Abstract The dissertation deals with cleaning and gas phase epitaxy in an ultra high vacuum multi chamber system UHV cluster tool for future CMOS technologies The emphasis of the work is the development and discussion of processes for the contact engineering of source and drain S/D for sub 100 nm MOSFETs Appropriate applications focus on the implementation of elevated S/D contacts to reduce S/D contact resistances or an epitaxial refilling of S/D extensions to minimize short channel effects In the context of this work for the first time a hydrogen plasma cleaning for the in situ LT cleaning of planar or pre structured substrates was combined with UHV CVD epitaxy in a cluster tool The use of a SiH₄/GeH₄ precursor gas mixture with typical process pressures of 10⁻⁴ mbar to 10⁻² mbar ensures selective epitaxial growth of silicon and silicon germanium alloys even without chlorine chemistry As a conclusion one can establish a full process including pre cleaning and selective epitaxial growth SEG at temperatures T

Granular Material-based Technologies Surajit Sen, Melany L. Hunt, Alan J. Hurd, 2003 The past decade has seen an explosive growth of interest in the physics of granular materials Granular media are comprised of macroscopic objects with internal degrees of freedom Granular assemblies exhibit phenomena that incorporate the properties of traditional solids liquids and gases making it a unique state In many ways the granular state is a generalized state of matter This new volume from the Materials Research Society brings together researchers from many countries and disciplines to assess issues and problems concerned with the collective behavior of granular assemblies as encountered in the context of various applications In particular these applications include ceramic science shock propagation and absorption design and maintenance of stable natural and artificial structures processing of pharmaceuticals and self assembly processes associated with the synthesis of ultrahigh density magnetic tapes The volume should be of value to researchers involved in exploring granular material based technologies and is expected to stimulate further research in this arena Topics include electromagnetic fields structural issues flow behavior and dynamical problems

The Fabrication and Characterization of MOSFETs with Titanium Dioxide and Hafnium Dioxide as Gate Dielectrics Tiezhong Ma, 2001 Positron Annihilation Werner Triftshauser, Werner Triftshäuser, Gottfried Kögel, Peter Sperr, 2001 When it comes to studying the structures and defects of materials there is presently no technique that is superior to positron annihilation The increasing demands for higher accuracy and reliability provide a constant stimulus to the field and the present book relates the newest and most important scientific discoveries made in the field of positron annihilation Istfa 2005 ASM International, 2005-01-01

Interfacial Engineering for Optimized Properties III: Volume 819 Christopher A. Schuh, 2004-07-16 The MRS Symposium Proceeding series is an internationally recognised reference suitable for researchers and practitioners

American Book Publishing Record, 2003 *Encyclopedia of Materials* K. H. J. Buschow, 2001 Accompanying CD ROM contains The Encyclopedia of Materials Science and Technology on a web access disc Defect and Impurity Engineered Semiconductors and Devices III: Volume 719 S. Ashok, 2002-08-09 This book focuses on the

deliberate introduction and manipulation of defects and impurities in order to engineer desired properties in semiconductor materials and devices In view of current exciting developments in wide bandgap semiconductors like GaN for blue light emission as well as high speed and high temperature electronics dopant and defect issues relevant to these materials are addressed Also featured are semiconductor nanocavities and nano structures with emphasis on the formation and impact of vacancy type defects Defect reaction problems pertaining to impurity gettering precipitation and hydrogen passivation are specific examples of defect engineering that improve the electronic quality of the material A number of papers also deal with characterization techniques needed to study and to identify defects in materials and device structures Finally papers also address issues such as interface control and passivation application of ion implantation plasma treatment and rapid thermal processing for creating activating suppressing trap levels and device applications *Diffusion and Defect Data* ,2000

Standard & Poor's Stock Reports ,2009-11

Mechanisms and Principles of Epitaxial Growth in Metallic

Systems: Volume 528 Luc T. Wille,1998-08-06 The MRS Symposium Proceeding series is an internationally recognised reference suitable for researchers and practitioners **Solid Freeform and Additive Fabrication: Volume 542** Duane Dimos,Stephen C. Danforth,Michael J. Cima,1999-04-27 The MRS Symposium Proceeding series is an internationally

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Table of Contents Silicon Front End Technology Materials P

1. Understanding the eBook Silicon Front End Technology Materials P
 - The Rise of Digital Reading Silicon Front End Technology Materials P
 - Advantages of eBooks Over Traditional Books
2. Identifying Silicon Front End Technology Materials P
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Silicon Front End Technology Materials P
 - User-Friendly Interface
4. Exploring eBook Recommendations from Silicon Front End Technology Materials P
 - Personalized Recommendations
 - Silicon Front End Technology Materials P User Reviews and Ratings
 - Silicon Front End Technology Materials P and Bestseller Lists
5. Accessing Silicon Front End Technology Materials P Free and Paid eBooks
 - Silicon Front End Technology Materials P Public Domain eBooks
 - Silicon Front End Technology Materials P eBook Subscription Services

- Silicon Front End Technology Materials P Budget-Friendly Options
- 6. Navigating Silicon Front End Technology Materials P eBook Formats
 - ePub, PDF, MOBI, and More
 - Silicon Front End Technology Materials P Compatibility with Devices
 - Silicon Front End Technology Materials P Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Silicon Front End Technology Materials P
 - Highlighting and Note-Taking Silicon Front End Technology Materials P
 - Interactive Elements Silicon Front End Technology Materials P
- 8. Staying Engaged with Silicon Front End Technology Materials P
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Silicon Front End Technology Materials P
- 9. Balancing eBooks and Physical Books Silicon Front End Technology Materials P
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Silicon Front End Technology Materials P
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Silicon Front End Technology Materials P
 - Setting Reading Goals Silicon Front End Technology Materials P
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Silicon Front End Technology Materials P
 - Fact-Checking eBook Content of Silicon Front End Technology Materials P
 - Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
- 14. Embracing eBook Trends

- Integration of Multimedia Elements
- Interactive and Gamified eBooks

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