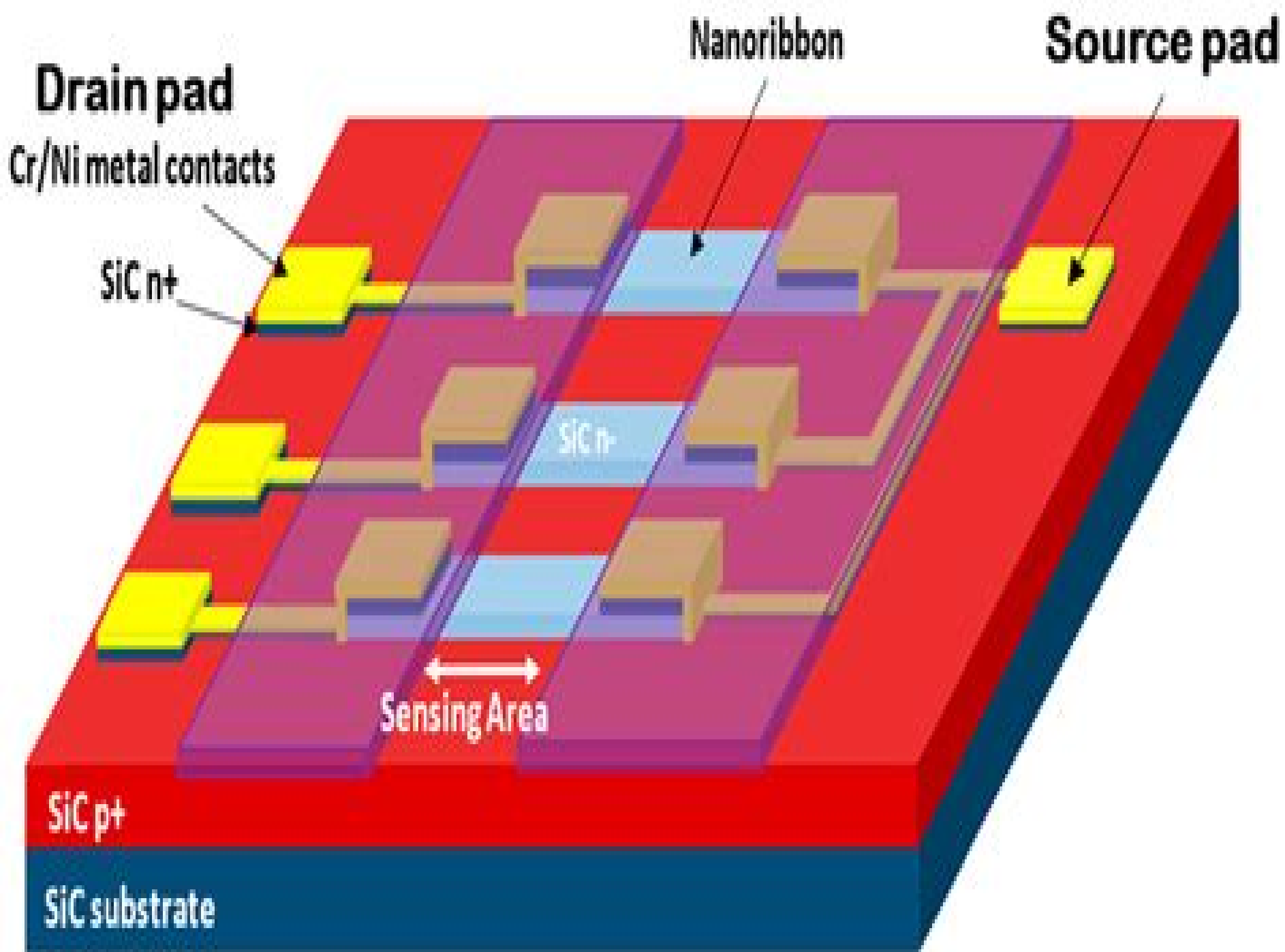




Silicon Frontend Junction Formationphysics And Technologies Proceedings

Terry C. Jones



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Reinigung und Gasphasenepitaxie in einem Ultrahochvakuum-Mehrkammersystem für zukünftige CMOS-Technologien
Anton Bayerstadler, 2006-09-14 Die Arbeit beschäftigt sich mit der Reinigung und Gasphasenepitaxie in einem Ultrahochvakuum Mehrkammersystem UHV Mehrkammersystem für zukünftige CMOS Technologien Der Schwerpunkt der Arbeit liegt dabei in der Entwicklung von Prozessen für das Kontakt Engineering von Source und Drain S D für sub 100 nm MOSFETs Mögliche Anwendungen liegen in der Implementierung von erhöhten S D Kontakten zur Erniedrigung der Source Drain Anschlusswiderstände oder dem epitaktischen Auffüllen von S D Extension Gebieten zur Minimierung von Kurzkanaleffekten Im Rahmen dieser Arbeit wurde zum ersten Mal in einem Clustertool eine Wasserstoff Plasmareinigung zur in situ LT Reinigung von planaren oder vorstrukturierten Substraten mit UHV CVD Epitaxie kombiniert Die Verwendung eines SiH₄ GeH₄ Prekursorengemisches erlaubt es bei den typischen Prozessdrücken von 10⁻⁴ mbar bis 10⁻² mbar auch ohne Chlorchemie selektive Abscheidung von Silizium und Silizium Germanium Legierungen zu gewährleisten Insgesamt lässt sich damit ein Gesamtprozess inklusive Vorreinigung und selektiver Abscheidung SEG bei Temperaturen T 800 °C für die Si UHV CVD und T 600 °C für die SiGe UHV CVD etablieren Die technologischen Rahmenbedingungen welche in der Entwicklung eines selektiven LT Epitaxieprozesses ohne chlorierte Prekursoren in Kombination mit einer in situ LT Reinigung in einem Clustertool gegeben sind wurden damit vollständig erfüllt Reinigung Es wurden grundlegende Untersuchungen von verschiedenen UHV kompatiblen Reinigungsverfahren die sich durch unterschiedliche Temperaturbereiche und die Reinigungseffizienz unterscheiden durchgeführt Ergebnisse aus Untersuchungen zur thermischen Desorption im UHV HT Reinigung konnten dabei auf die Wasserstoff Plasmareinigung LT Reinigung und die thermische Desorption in reaktiven Gasen LT Reinigung umgesetzt werden Das Hauptaugenmerk lag jeweils im Nachweis von Sauerstoff Kohlenstoff und Stickstoff Verunreinigungen Es zeigt sich dass die thermische Desorption bei technologisch relevanten Temperaturen im UHV äquivalent zur thermischen Desorption in Inertgasumgebungen z B dem sog H₂ bake ist Die H₂ Plasmareinigung ist mit einer maximalen Prozesstemperatur von ca 225 °C eine LT Reinigung Es konnte erstmals nachgewiesen werden dass die Zersetzung des Wasserstoffplasmas auf Oxid durch die Sputterwirkung von Ar Ionen initiiert wird Durch Ionenbeschuss wird das Oxid geschädigt und die obersten Si O Bindungen werden aufgebrochen Durch die H Radikale erfolgt schließlich ein ganzflächiger Oxidabtrag Gasphasenepitaxie Untersuchungen zur Morphologie von SiGe Schichten haben gezeigt dass sich im Gegensatz zur thermischen Desorption bei 900 °C mit H₂ Plasmareinigung als Vorreinigung schon bei 500 °C mittels UHV CVD glatte SiGe Schichten herstellen lassen Aufbauend auf den Ergebnissen zur ganzflächigen Epitaxie von Si und SiGe mittels UHV CVD wurde die Selektivität bezüglich vorstrukturierter Oxid und Nitridsubstrate untersucht Die Inkubationszeit für das Polywachstum auf Nitrid ist sehr viel geringer als für die Poly Nukleation auf Oxid Eine selektive Abscheidung von Si in Si Fenstern von vorstrukturierten Nitridsubstraten ist für UHV CVD bei 800 °C und thermischer Desorption als Vorreinigung

daher nicht möglich. Weitere Untersuchungen haben gezeigt, dass die Inkubationszeit vom Silan bzw. Germanpartialdruck abhängt. Zusätzlich wird das Nukleationsverhalten bei UHV CVD und H₂ Plasmareinigung als Vorreinigung durch die Plasmaparameter z. B. der Plasmadauer bestimmt. Prozesstechnisch kommt es bei Abscheidung im massentransportabhängigen Temperaturbereich bei Si UHV CVD z. B. bei 800 °C unabhängig von der Vorreinigung zu bekannten Effekten wie dem chemischen Loading Effekt und dem Facettenwachstum von Si (111) und Si (311). Mesaflächen. Beide Effekte sind prinzipiell unerwünscht. Zur elektrischen Charakterisierung der Kristallqualität von selektiv gewachsenen i-Si-Schichten wurden SEG-Mesen im Si-Fenster eines vorstrukturierten Oxidsubstrats gewachsen und zu einer n-p Diode prozessiert. Die bestimmte Volumendefektdichte entspricht der Dichte von ganzflächig abgeschiedenen intrinsischen Siliziumschichten. Glatte SiGe-Schichten mit einer ausreichenden maximalen selektiven Schichtdicke von ca. 30 nm konnten schließlich mit SiGe UHV CVD bei 500 °C in Kombination mit der H₂ Plasmareinigung als Vorreinigung hergestellt werden. Damit steht ein UHV-kompatibler Prozess zur Herstellung von vertikal erweiterten Source/Drain-Gebieten in planaren MOSFETs zur Verfügung. LP CVD ist für die Abscheidung selektiver SiGe-Schichten nicht geeignet, weshalb bei industriellen RP CVD-Prozessen SEG nur mit chlorierten Prekursoren bzw. durch Beimischung von HCl als Ztgas funktioniert. Die Anwendung des Prozesses zur Herstellung eines epitaktischen Source/Drain-Anschlussgebietes eines MOSFETs der 90 nm-Technologie wurde ebenfalls versucht. Das Ztzen der S/D-Extension erfolgt durch ein sequentielles H₂-Plasma Ztzen des Oxids mit anschließendem Si Ztzen. Das Wiederauffüllen der freien Ztzen-Extensionsgebiete ist aufgrund der gezeigten Selektivität zum Spacer-Material möglich. Abstract: The dissertation deals with cleaning and gas phase epitaxy in an ultra-high vacuum multi-chamber system. UHV cluster tool for future CMOS technologies. The emphasis of the work is the development and discussion of processes for the contact engineering of source and drain S/D for sub-100 nm MOSFETs. Appropriate applications focus on the implementation of elevated S/D contacts to reduce S/D contact resistances or an epitaxial refilling of S/D extensions to minimize short channel effects. In the context of this work, for the first time, a hydrogen plasma cleaning for the in situ LT cleaning of planar or pre-structured substrates was combined with UHV CVD epitaxy in a cluster tool. The use of a SiH₄/GeH₄ precursor gas mixture with typical process pressures of 10⁻⁴ mbar to 10⁻² mbar ensures selective epitaxial growth of silicon and silicon-germanium alloys even without chlorine chemistry. As a conclusion, one can establish a full process including pre-cleaning and selective epitaxial growth SEG at temperatures T.

Silicon Technologies Annie Baudrant, 2013-05-10. This book has been written to provide newly arrived engineers in a silicon foundry environment with a comprehensive background in the fundamental physical and chemical basis for major front-end silicon treatments such as oxidation, epitaxy, ion implantation and impurities diffusion, as well as giving a survey of the major types of equipment used in integrated circuit IC chip foundries. Techniques include various forms of chemical vapor deposition, CVD, epitaxy, thin film technologies, lithography, masking and other nanotechnologies. As well as the target audience, the book will also be of great interest to engineers and advanced students in

all these and related fields of electrical engineering materials science and manufacturing Comprehensive Semiconductor Science and Technology ,2011-01-28 Semiconductors are at the heart of modern living Almost everything we do be it work travel communication or entertainment all depend on some feature of semiconductor technology Comprehensive Semiconductor Science and Technology Six Volume Set captures the breadth of this important field and presents it in a single source to the large audience who study make and exploit semiconductors Previous attempts at this achievement have been abbreviated and have omitted important topics Written and Edited by a truly international team of experts this work delivers an objective yet cohesive global review of the semiconductor world The work is divided into three sections The first section is concerned with the fundamental physics of semiconductors showing how the electronic features and the lattice dynamics change drastically when systems vary from bulk to a low dimensional structure and further to a nanometer size Throughout this section there is an emphasis on the full understanding of the underlying physics The second section deals largely with the transformation of the conceptual framework of solid state physics into devices and systems which require the growth of extremely high purity nearly defect free bulk and epitaxial materials The last section is devoted to exploitation of the knowledge described in the previous sections to highlight the spectrum of devices we see all around us Provides a comprehensive global picture of the semiconductor world Each of the work s three sections presents a complete description of one aspect of the whole Written and Edited by a truly international team of experts *Amorphous and Nanocrystalline Silicon Science and Technology ...* ,2004 **Silicon Carbide 2004 - Materials, Processing and Devices: Volume 815** Materials Research Society. Meeting,2004-08-24 The MRS Symposium Proceeding series is an internationally recognised reference suitable for researchers and practitioners **Silicon Carbide 2004 - Materials, Processing and Devices: Volume 815** Michael Dudley,Perena Gouma,Tsunenobu Kimoto,Philip G. Neudeck,Stephen E. Saddow,2004-08-24 Silicon carbide SiC is a wide bandgap semiconductor that can operate at temperatures well above 300 C where silicon cannot perform In addition due to a high thermal conductivity equal to copper at room temperature SiC is an ideal candidate for operation in harsh environments and at high power levels Rapid advances in SiC materials and devices have recently resulted in implementation of SiC based electronic systems and the impact of these devices is expected to significantly increase in the next several years This book documents the most recent results on growth of bulk and epitaxial layers physical and structural properties process technology and device development obtained since the 10th International Conference on Silicon Carbide and Related Materials 2003 ICSCRM2003 held in Lyon France Extended defects in silicon carbide are highlighted The nature of defects induced by forward biasing of bipolar devices as well as methods to suppress the degradation are addressed

Amorphous and Nanocrystalline Silicon Science and Technology 2004: Volume 808 Materials Research Society. Meeting,2004-09-03 This book celebrates 20 years of MRS symposia on the topic of amorphous silicon Contributors showed that the simplified theories developed to explain the limited experimental information available in the early eighties have

spurred more sophisticated experimentation either refining the early understanding or making it irrelevant. The differences of opinion that continue to exist and emerge are probably the hallmark of the amazing vitality of this field. Applications range from mature thin film transistors, solar cells and image sensors to the emerging possibility of erbium doped nanocrystalline silicon based materials for lasers and amorphous silicon quantum dots for luminescent devices. The book discusses material characterization, growth processes and devices. Each chapter is further subdivided into sections that group papers around common themes. Topics include nanomaterials, electronic structure, metastable effects, understanding of growth processes, laser induced crystallization, metal induced crystallization, other growth techniques, newer devices, solar cells and thin film transistors. **Intergrative and Inerdisciplinary Aspects of Intermetallics: Volume 842** Materials Research Society. Meeting, 2005-06-02. The MRS Symposium Proceeding series is an internationally recognised reference suitable for

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chemistry, condensed matter physics, materials science and engineering, ceramics, chemical engineering and mineralogy, geology to name a few. The common challenge is to understand and to predict structures and properties of new materials. As with earlier volumes in this series from the Materials Research Society, the presentations here represent interdisciplinary research from around the world and explore not only recent advances in the solid state chemistry of inorganic materials but also their impact on commercial applications. The book covers a broad range of topics including synthesis and characterization of novel functional materials, design and fabrication of nanostructures and nanomaterials, crystal and structural chemistry, catalysis, gas separation and storage and magnetic and optical applications. Both theoretical and computational studies of solid state inorganic materials are featured. Joint presentations with solid state ionics are also particularly fruitful. **Progress in Compound Semiconductor Materials IV - Electronic and Optoelectronic**

Applications: Volume 829 Materials Research Society. Meeting, 2005-04-13. The MRS Symposium Proceeding series is an internationally recognised reference suitable for researchers and practitioners. *Characterization and Metrology for ULSI Technology 2005* David G. Seiler, 2005-09-29. The worldwide semiconductor community faces increasingly difficult challenges in the era of silicon nanotechnology and beyond. The magnitude of these challenges demands special attention from the metrology and analytical measurements community. New paradigms must be found. Adequate research and development for

new metrology concepts are urgently needed Characterization and metrology are key enablers for developing new semiconductor technology and in improving manufacturing This book summarizes major issues and gives critical reviews of important measurement techniques that are crucial to continuing the advances in semiconductor technology It covers major aspects of process technology and most characterization techniques for silicon research including development manufacturing and diagnostics The book also covers emerging nano devices and the corresponding metrology challenges that arise *Materials Issues in Art and Archaeology VII: Volume 852* Materials Research Society. Meeting, 2005-09-30 The MRS Symposium Proceeding series is an internationally recognised reference suitable for researchers and practitioners

Flexible Electronics--materials and Device Technology, 2004 **Integration of Advanced Micro- and Nanoelectronic Devices--critical Issues and Solutions** Materials Research Society. Meeting, 2004 This volume is the joint proceedings of papers presented in Symposium D High k Insulators and Ferroelectrics for Advanced Microelectronic Devices and Symposium E Integration Challenges in Next Generation Oxide Based Nanoelectronics held April 13 16 at the 2004 MRS Spring Meeting in San Francisco California p x *Solid-state Chemistry of Inorganic Materials*, 2005 **Materials Issues in Art and Archaeology**, 2005 *Materials for Photovoltaics: Volume 836* Materials Research Society. Meeting, 2005-09-09 Solar cell performance is critically dependent on the optical and electrical properties of their constituent materials In order to obtain significant improvements in performance for future generations of photovoltaic devices it will be necessary to either improve the properties of existing materials or engineer new materials and device structures This book focuses on materials issues and advances for photovoltaics Topics include dye sensitized solar cells nanoparticle hybrid solar cells polymer based devices small molecule based devices III V semiconductors II VI semiconductors and transparent conducting oxides and silicon thin films

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