

## **Architecture of RISC**

# **Risc Systems And Applications**

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programming model M88000 family and the 80 20 rule The text examines the selection of a microprocessor architecture changing design cycle semiconductor technology multiprocessing and real time software interrupts and exceptions Concerns include locating associated tasks MC88100 interrupt service routines single and multiple threaded operating systems and the MC68300 family The publication is a valuable reference for computer engineers and researchers interested in microprocessor architectures and systems *MCSE Training Guide* Erin Dunigan,1997 Solid information insider tips and the exclusive TestPrep test engine on the CD ROM make this the only book readers need to pass this popular elective exam The book provides readers with more than just exam basics it offers extra information on the technology itself

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