

**G. Wachutka
G. Schrag (eds.)**

**Simulation of Semiconductor
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Simulation of Semiconductor Processes and Devices 2004 Gerhard Wachutka, Gabriele Schrag, 2004-08-23 This volume contains the proceedings of the 10th edition of the International Conference on Simulation of Semiconductor Processes and Devices SISPAD 2004 held in Munich Germany on September 2-4 2004 The conference program included 7 invited plenary lectures and 82 contributed papers for oral or poster presentation which were carefully selected out of a total of 151 abstracts submitted from 14 countries around the world Like the previous meetings SISPAD 2004 provided a world wide forum for the presentation and discussion of recent advances and developments in the theoretical description physical modeling and numerical simulation and analysis of semiconductor fabrication processes device operation and system performance The variety of topics covered by the conference contributions reflects the physical effects and technological problems encountered in consequence of the progressively shrinking device dimensions and the ever growing complexity in device technology

Simulation of Semiconductor Processes and Devices 2004 Gerhard Wachutka, Gabriele Schrag, 2011-03-30

Physics of Semiconductor Devices Massimo Rudan, 2017-09-27 This textbook describes the basic physics of semiconductors including the hierarchy of transport models and connects the theory with the functioning of actual semiconductor devices Details are worked out carefully and derived from the basic physical concepts while keeping the internal coherence of the analysis and explaining the different levels of approximation Coverage includes the main steps used in the fabrication process of integrated circuits diffusion thermal oxidation epitaxy and ion implantation Examples are based on silicon due to its industrial importance Several chapters are included that provide the reader with the quantum mechanical concepts necessary for understanding the transport properties of crystals The behavior of crystals incorporating a position dependent impurity distribution is described and the different hierarchical transport models for semiconductor devices are derived from the Boltzmann transport equation to the hydrodynamic and drift diffusion models The transport models are then applied to a detailed description of the main semiconductor device architectures bipolar MOS CMOS including a number of solid state sensors The final chapters are devoted to the measuring methods for semiconductor device parameters and to a brief illustration of the scaling rules and numerical methods applied to the design of semiconductor devices

International Conference on Simulation of Semiconductor Processes and Devices, 2005

Nanoscale Semiconductor Memories Santosh K. Kurinec, Krzysztof Iniewski, 2017-07-28 Nanoscale memories are used everywhere From your iPhone to a supercomputer every electronic device contains at least one such type With coverage of current and prototypical technologies *Nanoscale Semiconductor Memories Technology and Applications* presents the latest research in the field of nanoscale memories technology in one place It also covers a myriad of applications that nanoscale memories technology has enabled The book begins with coverage of SRAM addressing the design challenges as the technology scales then provides design strategies to mitigate radiation induced upsets in SRAM It discusses the current state of the art DRAM

technology and the need to develop high performance sense amplifier circuitry The text then covers the novel concept of capacitorless 1T DRAM termed as Advanced RAM or A RAM and presents a discussion on quantum dot QD based flash memory Building on this foundation the coverage turns to STT RAM emphasizing scalable embedded STT RAM and the physics and engineering of magnetic domain wall racetrack memory The book also discusses state of the art modeling applied to phase change memory devices and includes an extensive review of RRAM highlighting the physics of operation and analyzing different materials systems currently under investigation The hunt is still on for universal memory that fits all the requirements of an ideal memory capable of high density storage low power operation unparalleled speed high endurance and low cost Taking an interdisciplinary approach this book bridges technological and application issues to provide the groundwork for developing custom designed memory systems

EDA for IC Implementation, Circuit Design, and Process Technology Luciano Lavagno, Louis Scheffer, Grant Martin, 2018-10-03 Presenting a comprehensive overview of the design automation algorithms tools and methodologies used to design integrated circuits the Electronic Design Automation for Integrated Circuits Handbook is available in two volumes The second volume EDA for IC Implementation Circuit Design and Process Technology thoroughly examines real time logic to GDSII a file format used to transfer data of semiconductor physical layout analog mixed signal design physical verification and technology CAD TCAD Chapters contributed by leading experts authoritatively discuss design for manufacturability at the nanoscale power supply network design and analysis design modeling and much more Save on the complete set

Recent Topics on Modeling of Semiconductor Processes, Devices, and Circuits Rasit Onur Topaloglu, Peng Li, 2011 The last couple of years have been very busy for the semiconductor industry and researchers The rapid speed of production channel length reduction has brought lithographic challenges to semiconductor modeling These include stress optimization transisto

Silicon-Germanium Heterojunction Bipolar Transistors for Mm-wave Systems Technology, Modeling and Circuit Applications Niccolò Rinaldi, Michael Schröter, 2022-09-01 The semiconductor industry is a fundamental building block of the new economy there is no area of modern life untouched by the progress of nanoelectronics The electronic chip is becoming an ever increasing portion of system solutions starting initially from less than 5% in the 1970 microcomputer era to more than 60% of the final cost of a mobile telephone 50% of the price of a personal computer representing nearly 100% of the functionalities and 30% of the price of a monitor in the early 2000 s Interest in utilizing the sub mm wave frequency spectrum for commercial and research applications has also been steadily increasing Such applications which constitute a diverse but sizeable future market span a large variety of areas such as health material science mass transit industrial automation communications and space exploration Silicon Germanium Heterojunction Bipolar Transistors for mm Wave Systems Technology Modeling and Circuit Applications provides an overview of results of the DOTSEVEN EU research project and as such focusses on key material developments for mm Wave Device Technology It starts with the motivation at the beginning of the project and a summary of

its major achievements The subsequent chapters provide a detailed description of the obtained research results in the various areas of process development device simulation compact device modeling experimental characterization reliability sub mm wave circuit design and systems

Electronic Design Automation for IC Implementation, Circuit Design, and Process Technology Luciano Lavagno,Igor L. Markov,Grant Martin,Louis K. Scheffer,2017-02-03 The second of two volumes in the Electronic Design Automation for Integrated Circuits Handbook Second Edition Electronic Design Automation for IC Implementation Circuit Design and Process Technology thoroughly examines real time logic RTL to GDSII a file format used to transfer data of semiconductor physical layout design flow analog mixed signal design physical verification and technology computer aided design TCAD Chapters contributed by leading experts authoritatively discuss design for manufacturability DFM at the nanoscale power supply network design and analysis design modeling and much more New to This Edition Major updates appearing in the initial phases of the design flow where the level of abstraction keeps rising to support more functionality with lower non recurring engineering NRE costs Significant revisions reflected in the final phases of the design flow where the complexity due to smaller and smaller geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting edge applications and approaches realized in the decade since publication of the previous edition these are illustrated by new chapters on 3D circuit integration and clock design Offering improved depth and modernity Electronic Design Automation for IC Implementation Circuit Design and Process Technology provides a valuable state of the art reference for electronic design automation EDA students researchers and professionals

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